D.N.R.COLLEGE OF ENGINEERING & TECHNOLOGY



Balusumudi Bhimavaram – 2 (Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada) (Accredited with B⁺⁺ Grade by NAAC) Ph: 08816-221238 Email: <u>dnrcet@gmail.com</u> website: <u>https://dnrcet.org</u> Electronics & Communication Engineering

COURSE OUTCOMES

ACADEMIC YEAR: 2020-21 NAME OF THE COURSE : Pulse & Digital Circuits Lab REGULATION: R16 YEAR/SEMESTER: III/I NAME OF THE FACULTY: K. S Satish Kumar

CO NO.	DESCRIPTION
C4316.1	Will be able to Generate and process sinusoidal and non- sinusoidal signals.
C4316.2	Will be able to understand fundamentals of basic logic gates and sampling gates.
C4316.3	Will be able to design the transistor as a switch circuits
C4316.4	Will be able to analyze various multivibrator circuits.
C4316.5	Will be able to analyze UJT relaxation oscillator and boot-strap sweep circuits

Signature of Faculty

Signature of HOD