



**D.N.R. COLLEGE OF ENGINEERING & TECHNOLOGY (Autonomous)
BALUSUMUDI, BHIMAVARAM, W.G. Dist., A.P., PIN-534 202**

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

DR-24

**COURSE STRUCTURE & SYLLABUS M.Tech ECE
Digital Electronics & Communication System (DECS)**

Program

(Applicable for Batch Admitted from 2024-25)



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I-SEMESTER

S. No	Course Code	Courses	Category	L	T	P	C
1	MT24DE1101	Program Core-1 Digital System Design	PC	3	0	0	3
2	MT24DE1102	Program Core-2 Digital Data Communications	PC	3	0	0	3
Program Elective-1							
3	MT24DE1103	Transform Techniques	PE	3	0	0	3
	MT24DE1104	VLSI Technology and Design	PE	3	0	0	3
	MT24DE1105	Radar Signal Processing	PE	3	0	0	3
Program Elective-2							
4	MT24DE1106	Statistical Signal Processing	PE	3	0	0	3
	MT24DE1107	Optical Communication Technology	PE	3	0	0	3
	MT24DE1108	Network Security & Cryptography	PE	3	0	0	3
6	MT24DE1109	Laboratory-1 System Design Using Verilog HDL Laboratory	LB	0	0	4	2
7	MT24DE1110	Laboratory-2 Data Communications Laboratory	LB	0	0	4	2
5	MT24DE1111	Research Methodology and IPR	CC			0	2
8	MT24DE1112	Audit Course 1*	AC	2	0	0	0
Total Credits							18

**Student has to choose any one Audit Course listed below.*



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II SEMESTER

S. No	Course Code	Courses	Category	L	T	P	C
1	MT24DE1201	Program Core-3 Image and Video Processing	PC	3	0	0	3
2	MT24DE1202	Program Core-4 Wireless Communications and Networks	PC	3	0	0	3
Program Elective-3							
3	MT24DE1203	CMOS Analog & Digital IC Design	PE	3	0	0	3
	MT24DE1204	Advanced Computer Architecture	PE	3	0	0	3
	MT24DE1205	Soft Computing Techniques	PE	3	0	0	3
Program Elective-4							
4	MT24DE1206	DSP Processors and Architectures	PE	3	0	0	3
	MT24DE1207	EMI/ EMC	PE	3	0	0	3
	MT24DE1208	Object Oriented Programming	PE	3	0	0	3
5	MT24DE1209	Laboratory-3 Advanced Communications Laboratory	LB	0	0	4	2
6	MT24DE1210	Laboratory-4 Advanced Digital Image & Video Processing Laboratory	LB	0	0	4	2
7	MT24DE1211	Mini Project	MP	2	0	0	2
8	MT24DE1212	Audit Course-2 *	AC	2	0	0	0
Total Credits							18

**Student has to choose any one audit course listed below.*



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III SEMESTER

S. No	Course Code	Courses	Category	L	T	P	C
Program Elective-5							
1	MT24DE2101	Detection & Estimation Theory	PE	3	0	0	3
	MT24DE2102	Advanced Digital Signal Processing	PE	3	0	0	3
	MT24DE2103	Coding Theory and Applications	PE	3	0	0	3
Open Elective							
2	MT24DE2104	Business Analytics	OE	3	0	0	3
	MT24DE2105	Industrial Safety	OE	3	0	0	3
	MT24DE2106	Operations Research	OE	3	0	0	3
	MT24DE2107	Cost Management of Engineering Projects	OE	3	0	0	3
	MT24DE2108	Composite Materials	OE	3	0	0	3
	MT24DE2109	Waste to Energy	OE	3	0	0	3
3	MT24DE2110	Dissertation Phase – I	PJ	0	0	20	10
Total Credits							16



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IV SEMESTER

S. No	Course Code	Courses	Category	L	T	P	C
1	MT24DE2201	Dissertation Phase – II	PJ	0	0	32	16
Total Credits							16

Audit Course 1 & 2:

1. English for Research Paper Writing
2. Disaster Management
3. Sanskrit for Technical Knowledge
4. Value Education
5. Constitution of India
6. Pedagogy Studies
7. Stress Management by Yoga
8. Personality Development through Life Enlightenment Skills



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year- I Semester	Course Code: MT24DE1101	L	T	P	C
		3	0	0	3
DIGITAL SYSTEM DESIGN					

OBJECTIVES:

The main objectives of this course are given below:

1. The basic concepts of K-map, tabular method, QM method are revised and higher order minimization techniques like CAMP algorithm and Cubical operations are explained.
2. PLA folding using COMPACT algorithms studied for various cases.
3. ASM charts are revised and design techniques of digital circuit realization are explained.
4. Digital system design is approached using CPLD, FPGA and ASIC.
5. Fault Diagnosis in Combinational Circuits are performed using various techniques like fault detection test, path sensitization method and Boolean difference method, Kohavi algorithm.
6. Fault Diagnosis in sequential circuits is performed using Circuit test approach, Hamming Experiments, synchronizing experiments, distinguishing and adaptive distinguishing experiments on different cases.

UNIT-I: Minimization Procedures and CAMP Algorithm:

Review on minimization of switching functions using tabular methods, k-map, QM algorithm, CAMP-I algorithm, Phase-I: Determination of Adjacencies, DA, CSC, SSMs and EPCs,, CAMP-I algorithm, Phase-II: Passport checking, Determination of SPC, CAMP-II algorithm: Determination of solution cube, Cube based operations, determination of selected cubes are wholly within the given switching function or not, Introduction to cube based algorithms.

UNIT-II: PLA Design, Minimization and Folding Algorithms:

Introduction to PLDs, basic configurations and advantages of PLDs, PLA-Introduction, Block diagram of PLA, size of PLA, PLA design aspects, PLA minimization algorithm (IISc algorithm), PLA folding algorithm (COMPACT algorithm)-Illustration of algorithms with suitable examples.

UNIT-III: Design of Large Scale Digital Systems:

Algorithmic state machine charts-Introduction, Derivation of SM Charts, Realization of SM Chart, control implementation, control unit design, data processor design, ROM design, PAL design aspects, digital system design approaches using CPLDs, FPGAs and ASICs.

UNIT-IV: Fault Diagnosis in Combinational Circuits:

Faults classes and models, fault diagnosis and testing, fault detection test, test generation, testing process, obtaining a minimal complete test set, circuit under test methods- Path sensitization method, Boolean difference method, properties of Boolean differences, Kohavi algorithm, faults in PLAs, DFT schemes, built in self-test.



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UNIT-V: Fault Diagnosis in Sequential Circuits:

Fault detection and location in sequential circuits, circuit test approach, initial state identification, Hamming experiments, synchronizing experiments, machine identification, distinguishing experiment, adaptive distinguishing experiments.

TEXTBOOKS:

1. Logic Design Theory-N. N.Biswas,PHI
2. Switching and Finite Automata Theory-Z. Kohavi, 2nd Edition, 2001,TMH
3. Digital system Design using PLDd - Lala

REFERENCE BOOKS:

1. Fundamentals of Logic Design –CharlesH.Roth,5th Ed.,Cengage Learning.
2. Digital Systems Testing and Testable Design – Miron Abramovici, Melvin A.Breuerand Arthur D.Friedman-John Wiley &Sons Inc.

OUTCOMES:

At the end of this course the student can able to:

1. Understand the basic concepts of a Karnaugh Map (“K-map”) for a 2-, 3-, 4-, or 5- variable logic function and to identify the prime implicates, essential prime implicates, and non essential prime implicates of a function depicted on a K-map.
2. Perform the minimization of a Boolean function using tabular method, QM algorithm and CAMP algorithm and determine the Adjacencies, DA, CSC, SSMs, EPCs and SPCs.
3. Perform the minimization of PLA using IISC algorithm and folding using COMPACT algorithm.
4. Can design a digital circuit by steps involving ASM chart.
5. Understand the digital system design approaches using CPLDs, FPGAs and ASICs.
6. Rectify a single fault and multiple faults in combinational circuits using Path sensitization method, Boolean difference method and Kohavi algorithm.
7. Perform fault diagnosis in sequential circuits.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – I Semester	Course Code: MT24DE1102	L	T	P	C
		3	0	0	3
DIGITAL DATA COMMUNICATIONS					

Course objectives:

The main objectives of this subject are:

1. Different modulation techniques to improve the band width and their properties.
2. Networking and different protocol systems.
3. Error estimation and correction, asynchronous and synchronous protocols.
4. Multiplexing techniques, different networking connections and interfacing devices.
5. Multiple access techniques and analysis.

UNIT-I:

Digital Modulation Schemes:

BPSK, QPSK, 8PSK, 16PSK, 8QAM, 16QAM, DPSK – Methods, Band Width Efficiency, Carrier Recovery, Clock Recovery.

UNIT-II:

Basic Concepts of Data Communications, Interfaces and Modems:

Data Communication Networks, Protocols and Standards, UART, USB, Line Configuration, Topology, Transmission Modes, Digital Data Transmission, DTE-DCE interface, Categories of Networks – TCP/IP Protocol suite and Comparison with OSI model.

UNIT-III:

Error Correction: Types of Errors, Vertical Redundancy Check (VRC), LRC, CRC, Checksum, Error Correction using Hamming code

Data Link Control: Line Discipline, Flow Control, Error Control

Data Link Protocols: Asynchronous Protocols, Synchronous Protocols, Character Oriented Protocols, Bit-Oriented Protocol, Link Access Procedures.

UNIT-IV:

Multiplexing: Frequency Division Multiplexing (FDM), Time Division Multiplexing (TDM), Multiplexing Application, and DSL.

Local Area Networks: Ethernet, Other Ether Networks, Token Bus, Token Ring, FDDI.

Metropolitan Area Networks: IEEE802.6, SMDS

Switching: Circuit Switching, Packet Switching, Message Switching.

Networking and Interfacing Devices: Repeaters, Bridges, Routers, Gateway, Other Devices

UNIT-V:

Multiple Access Techniques: Frequency- Division Multiple Access (FDMA), Time - Division Multiple Access (TDMA), Code - Division Multiple Access (CDMA), OFDM and OFDMA. Random Access, Aloha- Carrier Sense Multiple Access (CSMA)- Carrier Sense Multiple Access with Collision Avoidance (CSMA/CA), Controlled Access- Reservation- Polling- Token Passing, Channelization.



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TEXT BOOKS:

1. Data Communication and Computer Networking-B. A. Forouzan, 2nd Ed., 2003, TMH.
2. Advanced Electronic Communication Systems- W.Tomasi, 5th Ed., 2008, PEI.

REFERENCE BOOKS:

1. Data Communications and Computer Networks- Prakash C.Gupta, 2006, PHI.
2. Data and Computer Communications –William Stallings, 8th Ed., 2007, PHI.
3. Data Communication and Tele Processing Systems-T.Housely, 2nd Ed, 2008, BSP.
4. Data Communications and Computer Networks- Brijendra Singh, 2nd Ed., 2005, PHI.

Course outcomes:

At the end of this course the student can able to:

1. Model digital communication system using appropriate mathematical techniques (error probability, constellation diagrams, pharos diagrams).
2. Understanding the basic concepts of how digital data is transferred across computer networks.
3. Independently understand basic computer network technology.
4. Understand and explain Data Communications System and its components.
5. Identify the different types of network topologies and protocols.
6. Enumeratethe layers of the OSI model and TCP/IP. Explain the function(s) of each layer.
7. Identify the different types of network devices and their functions within a network
8. Understand and building the skills of subnetting and routing mechanisms.
9. Familiarity with the basic protocols of computer networks, and how they can be used
10. To assist in network design and implementation.



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I Year – I Semester	Course Code: MT24DE1103	L	T	P	C
		3	0	0	3
TRANSFORM TECHNIQUES (ELECTIVE – I)					

UNIT-I:

Fourier Analysis:

Fourier series, Examples, Fourier Transform, Properties of Fourier Transform, Examples of Fourier transform, sampling theorem, Partial sum and Gibbs phenomenon, Fourier analysis of Discrete time Signals, Discrete Fourier Transform.

Time–Frequency Analysis: Window function, Short Time Fourier Transform, Discrete Short Time

Fourier Transform, Continuous wavelet transform, discrete wavelet transform, wavelet series, Interpretations of the Time-Frequency plot.

UNIT-II:

Transforms:

Walsh, Hadamard, Haar and Slant Transforms, DCT, DST, KLT, Singular value Decomposition – definition, properties and applications

UNIT-III:

Continuous Wavelet Transform (CWT):

Shortcomings of STFT, Need for wavelets, Wavelet Basis- Concept of Scale and its relation with frequency, Continuous time wavelet Transform Equation- Series Expansion using Wavelets-CWT- Tiling of time scale plane for CWT. Important Wavelets: Haar, Mexican Hat, Meyer, Shannon, Daubechies.

UNIT-IV:

MultiRate Analysis and DWT:

Need for Scaling function – Multi Resolution Analysis, Two-Channel Filter Banks, Perfect Reconstruction Condition, Relationship between Filter Banks and Wavelet Basis, DWT, Structure of DWT Filter Banks, Daubechies Wavelet Function, Applications of DWT.

UNIT-V:

Wavelet Packets and Lifting: Wavelet Packet Transform, Wavelet packet algorithms, Thresholding- Hard thresholding, Soft thresholding, Multidimensional Wavelets, Bi-orthogonal basis- B- Splines, Lifting Scheme of Wavelet Generation, Multi Wavelets



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TEXTBOOKS:

1. A Wavelet Tour of Signal Processing theory and applications- Raghuveer M. Rao and Ajit S. Bopardikar, Pearson Edu, Asia, New Delhi, 2003.
2. K.P. Soman and K.I Rama chandran, "In sight into Wavelets—from theory to practice" PHI, Second edition, 2008

REFERENCEBOOKS:

1. Fundamentals of Wavelets -Theory,Algorithms and Applications –Jaideva C Goswami, Andrew K Chan, John Wiley & Sons, Inc, Singapore, 1999.
2. Jaideva C.Goswamiand Andrew K.Chan,"Fundamentals of Wavelets"Wiley publishers,2006
3. A Wavelet Tour of Signal Processing - Stephen G.Mallat, Academic Press, 2Ed
4. Digital Image Processing– S. Jayaraman, S.Esakkirajan,T.VeeraKumar–TMH,2009

Course Outcomes:

On completion of this course student will be able to:

1. The student will learn basics of two-dimensional transforms.
2. Understand the definition, properties and applications of various two-dimensional transform.
3. Understand the basic concepts of wavelet transform.
4. Understand the special topics such as wavelet packets, Bi-orthogonal wavelets e.t.c.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – I Semester	Course Code: MT24DE1104	L	T	P	C
		3	0	0	3
VLSI TECHNOLOGY AND DESIGN (ELECTIVE – I)					

UNIT-I:

VLSI Technology: Fundamentals and applications, IC production process, semiconductor processes, design rules and process parameters, layout techniques and process parameters.

VLSI Design: Electronic design automation concept, ASIC and FPGA design flows, SOC designs, design technologies: combinational design techniques, sequential design techniques, state machine logic design techniques and design issues.

UNIT-II:

CMOS VLSI Design: MOS Technology and fabrication process of pMOS, nMOS, CMOS and BiCMOS technologies, comparison of different processes.

Building Blocks of a VLSI circuit: Computer architecture, memory architectures, communication interfaces, mixed signal interfaces.

VLSI Design Issues: Design process, design for testability, technology options, power calculations, package selection, clock mechanisms, mixed signal design.

UNIT-III:

Basic electrical properties of MOS and BiCMOS circuits, MOS and Bi CMOS circuit design processes, Basic circuit concepts, scaling of MOS circuits- qualitative and quantitative analysis with proper illustrations and necessary derivations of expressions.

UNIT-IV:

Sub system Design and Layout: Some architectural issues, switch logic, gate logic, examples of structured design (combinational logic), some clocked sequential circuits, other system considerations. **Sub system Design Processes:** Some general considerations and an illustration of design processes, design of an ALU subsystem.

UNIT-V:

Floor Planning: Introduction, Floor planning methods, off-chip connections.

Architecture Design: Introduction, Register-Transfer design, high-level synthesis, architectures for low power, architecture testing.

Chip Design: Introduction and design methodologies.



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TEXTBOOKS:

1. Essentials of VLSI Circuits and Systems, K.Eshraghian, Douglas A.Pucknell, Sholeh Eshraghian, 2005, PHI Publications.
2. Modern VLSI Design-Wayne Wolf, 3rd Ed., 1997, Pearson Education.
3. VLSI Design- Dr.K.V.K.K. Prasad, Kattula Shyamala, Kogent Learning Solutions Inc., 2012.

REFERENCE BOOKS:

1. VLSI Design Technologies for Analog and Digital Circuits, Randall L.Geiger, Phillip E.Allen, Noel R.Strader, TMH Publications, 2010.
2. Introduction to VLSI Systems: A Logic, Circuit and System Perspective -Ming-BOLin, CRC Press, 2011.
3. Principals of CMOS VLSI Design-N.H.EWeste, K.Eshraghian, 2nd Edition, Addison Wesley.

Course Outcomes

1. Review of FET fundamentals for VLSI design.
2. To acquires knowledge about stick diagrams and layouts.
3. Enable to design the subsystems based on VLSI concepts.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – I Semester	Course Code: MT24DE1105	L	T	P	C
		3	0	0	3
RADAR SIGNAL PROCESSING (ELECTIVE-I)					

Core Objectives:

The main objectives of this subject are:

1. Derivation of Radar range and Design of matched filter for different noises.
2. Signal detection techniques at receiver.
3. Optimum Radar Waveforms for Detection of signals in Clutter and various Families.
4. The characteristics of a Linear pulse and digital compression to Radar signals.
5. The principles of different phase coding techniques and analysis.

UNIT-I:

Introduction:

Radar Block Diagram, Bistatic Radar, Monostatic Radar, Radar Equation, Information Available from Radar Echo. Review of Radar Range Performance– General Radar Range Equation, Radar Detection with Noise Jamming, Beacon and Repeater Equations, MTI and Pulse Doppler Radar. Matched Filter Receiver – Impulse Response, Frequency Response Characteristic and its Derivation, Matched Filter and Correlation Function, Correlation Detection and Cross-Correlation Receiver, Efficiency of Non-Matched Filters, Matched Filter for Non-White Noise.

UNIT-II:

Detection of Radar Signals in Noise:

Detection Criteria – Neyman-Pearson Observer, Likelihood-Ratio Receiver, Inverse Probability Receiver, Sequential Observer, Detectors–Envelope Detector, Logarithmic Detector, I/Q Detector. Automatic Detection-CFAR Receiver, Cell Averaging CFAR Receiver, CFAR Loss, CFAR Uses in Radar. Radar Signal Management–Schematics, Component Parts, Resources and Constraints.

UNIT-III:

Waveform Selection[3,2]:

Radar Ambiguity Function and Ambiguity Diagram – Principles and Properties; Specific Cases – Ideal Case, Single Pulse of Sine Wave, Periodic Pulse Train, Single Linear FM Pulse, Noise Like Waveforms, Waveform Design Requirements, Optimum Waveforms for Detection in Clutter, Family of Radar Waveforms.

UNIT-IV:

Pulse Compression in Radar Signals:

Introduction, Significance, Types, Linear FM Pulse Compression – Block Diagram, Characteristics, Reduction of Time Side lobes, Stretch Techniques, Generation and Decoding of FM Waveforms – Block Schematic and Characteristics of Passive System, Digital Compression, SAW Pulse Compression.



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UNIT-V:

Phase Coding Techniques:

Principles, Binary Phase Coding, Barker Codes, Maximal Length Sequence (MLS/LRS/PN), Block Diagram of a Phase Coded CW Radar.

Poly Phase Codes : Frank Codes, Costas Codes, Non-Linear FM Pulse Compression, Doppler Tolerant PC Waveforms – Short Pulse, Linear Period Modulation (LPM/HFM), Sidelobe Reduction for Phase Coded PC Signals.

TEXT BOOKS:

1. Radar Handbook-M.I.Skolnik, 2nd Ed.,1991, McGraw Hill.
2. Radar Design Principles: Signal Processing and The Environment –Fred E.Nathanson, 2ndEd., 1999, PHI.
3. Introduction to Radar Systems-M.I.Skolnik, 3rd Ed., 2001, TMH.

REFERENCEBOOKS:

1. Radar Principles -PeytonZ.Peebles, Jr.,2004, JohnWiley.
2. Radar Signal Processing and Adaptive Systems- R.Nitzberg, 1999, ArtechHouse.

Core Outcomes:

At the end of this course the student can able to:

1. Understand the operation of Radar and characteristics of Matched filter for non-white noise.
2. Understand the various detection criterion and types of detectors that can be used to detect the Radar signals in noise.
4. Understand the wave form design requirements and optimum waveforms for the detection of signals in clutter.
5. Know the significance and types of pulse compression techniques for analog and digital signals.
6. Understand the requirements of phase coding in Radar and various polyphase codes used for phase coding.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – I Semester	Course Code: MT24DE1106	L	T	P	C
		3	0	0	3
STATISTICAL SIGNAL PROCESSING (ELECTIVE - II)					

UNIT-I

Signal models and characterization: Types and properties of statistical models for signals and how they relate to signal processing, Common second-order methods of characterizing signals including autocorrelation, partial correlation, cross-correlation, power spectral density and cross-power spectral density.

UNIT- II

Spectral estimation: Nonparametric methods for estimation of power spectral density, autocorrelation, cross-correlation, transfer functions, and coherence from finite signal samples.

UNIT-III

Review of signal processing: A review on random processes, A review on filtering random processes, Examples. **Statistical parameter estimation:** Maximum likelihood estimation, maximum a posteriori estimation, Cramer-Rao bound.

UNIT-IV

Eigen structure based frequency estimation: Pisarenko, MUSIC, ESPRIT their application sensor array direction finding.

Spectrum estimation: Moving average (MA), Auto Regressive (AR), Auto Regressive Moving Average (ARMA), Various non-parametric approaches.

UNITV

Wiener filtering: The finite impulse case, causal and non-causal infinite impulse responses cases, Least mean squares adaptation, recursive least squares adaptation, Kalman filtering.

TEXTBOOKS:

1. Steven M. Kay, fundamentals of statistical signal processing: estimation Theory, Prentice-Hall, 1993.
2. Monsoon H. Hayes, Stastical digital signal processing and modeling, USA, Wiley, 1996.

REFERENCE BOOKS:

1. Dimitris G. Manolakis, Vinay K. Ingle, and Stephen M. Kogon, Statistical and adaptive signal processing, Artech House, Inc, 2005, ISBN 1580536107

Course Outcomes:

1. Analyse signals and develop their statistical models for efficient processing
2. Formulate filtering problems from real life applications and design filtering solutions to estimate a desired signal from a given mixture by minimizing a cost function
3. Design and analyse efficient algorithms for estimation of various parameters of signals with different constraints
4. Develop efficient methods for spectrum and frequency estimation suiting the requirements derived



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I Year – I Semester	Course Code: MT24DE1107	L	T	P	C
		3	0	0	3
OPTICAL COMMUNICATION TECHNOLOGY (ELECTIVE-II)					

Course Objectives

1. To expose the students to the basics of signal propagation through optical fibers ,fiber impairments
2. Students should be familiar with commonly used components and sub systems in optical communication and network systems
3. To know the Optical Modulation and demodulation and Error Detection and Correction codes.
4. Learn about optical amplifier Transmission system model, power penalty-transmitter, power penalty-transmitter, receiver Scope – receiver optical amplifiers, crosstalk, dispersion,
5. Learn about necessity of wavelength division multiplexing (WDM), working principle and techniques of multiplexing, and Overall System Design considerations and optical networks

UNIT-I:

Signal propagation in Optical Fibers:

Geometrical Optics approach and Wave Theory approach, Loss and Bandwidth, Chromatic Dispersion, Non Linear effects- Stimulated Brillouin and Stimulated Raman Scattering, Propagation in a Non-Linear Medium, Self-Phase Modulation and Cross Phase Modulation, Four Wave Mixing, Principle of Solitons.

UNIT –II:

Fiber Optic Components for Communication & Networking:

Couplers, Isolators and Circulators, Multiplexers, Bragg Gratings, Fabry-Perot Filters, Mach Zender Interferometers, Arrayed Waveguide Grating, Tunable Filters, High Channel Count Multiplexer Architectures, Optical Amplifiers, Direct and External Modulation Transmitters, Pump Sources for Amplifiers, Optical Switches and Wavelength Converters.

UNIT –III:

Modulation and Demodulation:

Signal formats for Modulation, Subcarrier Modulation and Multiplexing, Optical Modulations – Duobinary, Single Side Band and Multilevel Schemes, Ideal and Practical receivers for Demodulation, Bit Error Rates, Timing Recovery and Equalization, Reed-Solomon Codes for Error Detection and Correction.

UNIT-IV:

Transmission System Engineering:

System Model, Power Penalty in Transmitter and Receiver, Optical Amplifiers, Crosstalk and Reduction of Crosstalk, Cascaded Filters, Dispersion Limitations and Compensation Techniques.

UNIT –V:

Fiber Non-linearities and System Design Considerations:

Limitation in High Speed and WDM Systems due to Non-linearities in Fibers, Wavelength Stabilization against Temperature Variations, Overall System Design considerations – Fiber Dispersion, Modulation, Non-Linear Effects, Wavelengths, All Optical Networks.



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TEXT BOOKS:

1. Optical Networks: A Practical Perspective - Rajiv Ramaswami and Kumar N. Sivarajan, 2nd Ed., 2004, Elsevier Morgan Kaufmann Publishers (An Im print of Elsevier).
2. Optical Fiber Communications – Gerd Keiser, 3rd Ed., 2000, McGraw Hill.

REFERENCE BOOKS:

1. Optical Fiber Communications: Principles and Practice – John.M.Senior, 2nd Ed., 2000, PE.
2. Fiber Optics Communication – Harold Kolimbris, 2nd Ed., 2004, PEI
3. Optical Networks : Third Generation Transport Systems – Uyles Black, 2nd Ed., 2009, PEI
4. Optical Fiber Communications – Govind Agarwal, 2nd Ed., 2004, TMH.
5. Optical Fiber Communications and Its Applications – S.C.Gupta, 2004, PHI.
6. Optical Networks : Third Generation Transport Systems – Uyles Black, 2nd Ed., 2009, PEI
7. Optical Fiber Communications – Govind Agarwal, 2nd Ed., 2004, TMH.
8. Optical Fiber Communications and Its Applications – S.C.Gupta, 2004, PHI.

Course outcomes

At the end of this course the student can able to:

1. Able to analyze characteristics of optical fiber and signal propagation through optical fibers Know the commonly used components and subsystems in optical communication and Network systems, working principle of optical communication components, amplifiers, filters
2. Able to analyze Transmission system model
3. Understand the importance of wavelength division multiplexing (WDM) and de-multiplexing,



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – I Semester	Course Code: MT24DE1108	L	T	P	C
		3	0	0	3
NETWORK SECURITY AND CRYPTOGRAPHY (ELECTIVE -II)					

UNIT-I:

Introduction:

Attacks, Services and Mechanisms, Security attacks, Security services, A Model for Internetwork security. Classical Techniques: Conventional Encryption model, Steganography, Classical Encryption Techniques.

Modern Techniques:

Simplified DES, Block Cipher Principles, Data Encryption standard, Strength of DES, Differential and Linear Cryptanalysis, Block Cipher Design Principles and Modes of operations.

UNIT-II:

Encryption Algorithms:

Triple DES, International Data Encryption algorithm, Blowfish, RC5, CAST-128, RC2, Characteristics of Advanced Symmetric block ciphers.

Conventional Encryption: Placement of Encryption function, Traffic confidentiality, Key distribution, Random Number Generation.

UNIT-III:

Public Key Cryptography: Principles, RSA Algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Cryptography.

Number Theory: Prime and Relatively prime numbers, Modular arithmetic, Fermat's and Euler's theorems, Testing for primality, Euclid's Algorithm, the Chinese remainder theorem, Discrete logarithms.

UNIT-IV:

Message Authentication and Hash Functions: Authentication requirements and functions,

Message Authentication, Hash functions, Security of Hash functions and MACs.

Hash and Mac Algorithms

MD File, Message digest Algorithm, Secure Hash Algorithm, RIPEMD-160, HMAC. Digital signatures and Authentication protocols: Digital signatures, Authentication Protocols, Digital signature standards.

Authentication Applications: Kerberos, X.509 directory Authentication service. Electronic Mail Security: Pretty Good Privacy, S/MIME.

UNIT –V:

IP Security:

Overview, Architecture, Authentication, Encapsulating Security Payload, Combining security Associations, Key Management. Web Security: Web Security requirements, Secure sockets layer and Transport layer security, Secure Electronic Transaction.

Intruders, Viruses and Worms Intruders, Viruses and Related threats.

FireWalls: Firewall Design Principles, Trusted systems.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

TEXT BOOKS:

1. Network Security: The complete reference, Robert Bragg, Mark Rhodes, TMH
2. Introduction to Cryptography, Buchmann, Springer.

Course Outcomes:

At the end of the course, students will be able to:

1. Identify and utilize different forms of cryptography techniques.
2. Incorporate authentication and security in the network applications.
3. Distinguish among different types of threats to the system and handle the same.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – I Semester	Course Code: MT24DE1109	L	T	P	C
		0	0	4	2
SYSTEM DESIGN USING VERILOG HDL LABORATORY					

List of Experiments:

- 1) Verilog implementation of 8:1 Mux/Demux, Full Adder, 8-bit Magnitude comparator, Encoder/decoder, Priority encoder, D-FF, 4-bit Shift registers (SISO, SIPO, PISO, bidirectional),
- 2) 3-bit Synchronous Counters, Binary to Gray converter, Parity generator.
- 3) Sequence generator/detectors, Synchronous FSM–Mealy and Moore machines.
- 4) Vending machines-Traffic Light controller, ATM, elevator control.
- 5) PCI Bus & arbiter and downloading on FPGA.
- 6) UART/USART implementation in Verilog.
- 7) Realization of single port SRAM in Verilog.
- 8) Verilog implementation of Arithmetic circuits like serial adder/subtractor, parallel adder/subtractor, serial/parallel multiplier.
- 9) Discrete Fourier transform/Fast Fourier Transform algorithm in Verilog.

Course Outcomes:

At the end of the laboratory work, students will be able to:

1. Identify, formulate, solve and implement problems in signal processing, communication systems etc using RTL design tools.
2. Use EDA tools like Cadence, Mentor Graphics and Xilinx.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – I Semester	Course Code: MT24DE1110	L	T	P	C
		0	0	4	2
DATA COMMUNICATIONS LAB					

List of Experiments:

1. Study of serial interface RS-232
2. Study of pc to pc communication using parallel port
3. To establish pc-pc communication using LAN
4. Study of LAN using star topology, bus topology and tree topology
5. Study and configure modem of a computer
6. To configure a hub/switch
7. To study the interconnections of cables for data communication
8. Study of a wireless communication system
9. Setup of time division multiplexing using fiber optics
10. Digital Fiber Optical Transmitter and Receiver



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – I Semester	Course Code: MT24DE1110	L	T	P	C
		0	0	4	2
RESEARCH METHODOLOGY AND IPR					

UNIT-I:

Meaning of research problem, Sources of research problem, Criteria Characteristics of a good research problem, Errors in selecting a research problem, Scope and objectives of research problem. Approaches of investigation of solutions for research problem, data collection, analysis, interpretation, Necessary instrumentations

UNIT-II:

Effective literature studies approaches, analysis Plagiarism , Research ethics,

UNIT-III:

Effective technical writing, how to write report, Paper Developing a Research Proposal, Format of research proposal, a presentation and assessment by a review committee

UNIT-IV:

Nature of Intellectual Property: Patents, Designs, Trade marks and Copyright. Process of Patenting and Development: technological research, innovation, patenting, development.

International Scenario: International cooperation on Intellectual Property.
Procedure for grants of patents, Patenting under PCT.

UNIT-V:

Patent Rights: Scope of Patent Rights. Licensing and transfer of technology. Patent information and databases. Geographical Indications.

UNIT-VI:

New Developments in IPR: Administration of Patent System. New developments in IPR; IPR of Biological Systems, Computer Software etc. Traditional knowledge Case Studies, IPR and IITs.

References:

1. Stuart Melville and Wayne Goddard, “Research methodology: an introduction for science & engineering students”
2. Wayne Goddard and Stuart Melville, “ Research Methodology: An Introduction”
3. Ranjit Kumar, 2nd Edition , “Research Methodology: A Step by Step Guide for beginners”
4. Halbert, “ Resisting Intellectual Property”, Taylor & Francis Ltd ,2007.
5. Mayall, “ Industrial Design”, Mc GrawHill,1992.
6. Niebel, “Product Design”, McGrawHill,1974.
7. Asimov, “Introduction to Design”, Prentice Hall,1962.
8. Robert P.Merges, Peter S.Menell, Mark A.Lemley, “Intellectual Property in New Technological Age”, 2016.
9. T.Ramappa, “Intellectual Property Rights Under WTO”, S.Chand, 2008.



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Course Outcomes:

At the end of this course, students will be able to

1. Understand research problem formulation.
2. Analyze research related information
3. Follow research ethics
4. Understand that today's world is controlled by Computer, Information Technology, but tomorrow world will be ruled by ideas, concept, and creativity.
5. Understanding that when IPR would take such important place in growth of individuals & nation, it is needless to emphasize the need of information about Intellectual Property Right to be promoted among students in general & engineering in particular.
6. Understand that IPR protection provides an incentive to inventors for further research work and investment in R & D, which leads to creation of new and better products, and in turn brings about, economic growth and social benefits.



I Year – II Semester	Course Code: MT24DE1201	L	T	P	C
		3	0	0	3
IMAGE AND VIDEO PROCESSING					

Course objectives:

- The basic concepts and methods to develop foundation in digital image processing and video processing are introduced and The Importance of various image transforms, image transform properties are discussed.
- Understanding the image enhancement techniques in both spatial domain and frequency domain.
- The process of recovering image that has been degraded by noise or any other degradation phenomenon.
- Understanding the importance of image segmentation and various methods used for segmentation, The importance of reducing the data for digital image representation by using various image compression techniques
- To understand the importance of video processing in multimedia and the various video formation models, motion estimation techniques in video processing
- Applications of motion estimation in video processing

UNIT–I:

Fundamentals of Image Processing and Image Transforms:

Introduction, Image sampling, Quantization, Resolution, Image file formats, Elements of image processing system, Applications of Digital image processing Introduction, Need for transform, image transforms, Fourier transform, 2 D Discrete Fourier transform and its transforms, Importance of phase, Walsh transform, Hadamard transform, Haar transform, slant transform Discrete cosine transform, KL transform, singular value decomposition, Radon transform, comparison of different image transforms.

UNIT–II:

Image Enhancement:

Spatial domain methods: Histogram processing, Fundamentals of Spatial filtering, Smoothing spatial filters, Sharpening spatial filters. Frequency domain methods: Basics of filtering in frequency domain, image smoothing, image sharpening, Selective filtering.

Image Restoration:

Introduction to Image restoration, Image degradation, Types of image blur, Classification of image restoration techniques, Image restoration model, Linear and Nonlinear image restoration techniques, Blind de-convolution

UNIT–III:

Image Segmentation: Introduction to image segmentation, Point, Line and Edge Detection, Region based segmentation., Classification of segmentation techniques, Region approach to image segmentation, clustering techniques, Image segmentation based on thresholding, Edge based segmentation, Edge detection and linking, Hough transform, Active contour

Image Compression:

Introduction, Need for image compression, Redundancy in images, Classification of redundancy in images, image compression scheme, Classification of image compression schemes, Fundamentals of information theory, Run length coding, Shannon – Fano coding, Huffman coding, Arithmetic coding, Predictive coding, Transformed based



compression, Image compression standard, Wavelet-based image compression, JPEG Standards.

UNIT-IV:

Basic Steps of Video Processing:

Analog Video, Digital Video. Time-Varying Image Formation models: Three-Dimensional Motion Models, Geometric Image Formation, Photometric Image Formation, Sampling of Video signals, Filtering operations.

UNIT-V:

2-D Motion Estimation:

Optical flow, General Methodologies, Pixel Based Motion Estimation, Block-Matching Algorithm, Mesh based Motion Estimation, Global Motion Estimation, Region based Motion Estimation, Multi resolution motion estimation, Waveform based coding, Block based transform coding, Predictive coding, Application of motion estimation in Video coding.

TEXT BOOKS:

1. Digital Image Processing–Gonzalez and Woods, 3rd Ed., Pearson.
2. Video Processing and Communication – Yao Wang, Joem Ostermann and Yaquin Zhang. 1st Ed., PH Int.
3. S.Jayaraman, S.Esakirajan and T.VeeraKumar, “Digital Image processing, Tata McGraw Hill publishers, 2009

REFERENCE BOOKS:

1. Digital Image Processing and Analysis Human and Computer Vision Application with CVIP Tools – Scotte Umbaugh, 2nd Ed, CRC Press, 2011.
2. Digital Video Processing–M.Tekalp, Prentice Hall International.
3. Multi dimensional Signal, Image and Video Processing and Coding – John Woods, 2nd Ed, Elsevier.
4. Digital Image Processing with MATLAB and Lab view –Vipula Singh, Elsevier.
5. Video Demystified –A Hand Book for the Digital Engineer–KeithJack,5thEd., Elsevier.

Course Outcomes

1. Know digital image, representation of digital image, importance of image resolution, applications in image processing, the advantages of representation of digital images in transform domain, application of various image transforms. Understand and analyze the image enhancement and image degradation, image restoration techniques using spatial filters and frequency domain.
2. Understand and analyze the detection of point, line and edges in images, edge linking and various segmentation techniques and the redundancy in images, various image compression techniques.
3. Describe the video technology from analog color TV systems to digital video systems, how video signal is sampled and filtering operations in video processing.
4. Describe the general methodologies for 2D motion estimation, various coding used in video processing.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1202	L	T	P	C
		3	0	0	3
WIRELESS COMMUNICATIONS AND NETWORKS					

OBJECTIVES:

1. The Aim of this course is to introduce the fundamental technologies for wireless communications and networking.
2. It introduces the Key concepts of Cellular and Mobile communications.
3. Introducing the concepts of Multiple Access Schemes.
4. Introducing the important concepts of Wireless networking, WLAN, WLL, IEEE802 standards.

UNIT-I:

The Cellular Concept-System Design Fundamentals:

Introduction, Frequency Reuse, Interference and system capacity – Co channel Interference and system capacity, Channel planning for Wireless Systems, Adjacent Channel interference , Power Control for Reducing interference, Improving Coverage & Capacity in Cellular Systems- Cell Splitting, Sectoring, Channel Assignment Strategies, Handoff Strategies- Prioritizing Handoffs, Practical Handoff Considerations, Trunking and Grade of Service

UNIT –II: Mobile Radio Propagation: Large-Scale Path Loss: Introduction to Radio Wave Propagation, Free Space Propagation Model, Relating Power to Electric Field, Basic Propagation Mechanisms, Reflection: Reflection from Dielectrics, Brewster Angle, Reflection from perfect conductors, Ground Reflection (Two- Ray) Model, Diffraction: Fresnel Zone Geometry, Knife-edge Diffraction Model, Multiple knife-edge Diffraction, Scattering, Outdoor Propagation Models- Longley-Ryce Model, Okumura Model, Hata Model, PCS Extension to Hata Model, Walfisch and Bertoni Model, Wideband PCS Microcell Model, Indoor Propagation Models- Partition losses (Same Floor), Partition losses between Floors, Log-distance path loss model, Ericsson Multiple Breakpoint Model, Attenuation Factor Model, Signal penetration into buildings, Ray Tracing and Site Specific Modeling.

UNIT –III:

Mobile Radio Propagation: Small-Scale Fading and Multipath

Small Scale Multipath propagation-Factors influencing small scale fading, Doppler shift, Impulse Response Model of a multipath channel- Relationship between Bandwidth and Received power, Small- Scale Multipath Measurements-Direct RF Pulse System, Spread Spectrum Sliding Correlator Channel Sounding, Frequency Domain Channels Sounding, Parameters of Mobile Multipath Channels-Time Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time, Types of Small- Scale Fading-Fading effects Due to Multipath Time Delay Spread, Flat fading, Frequency selective fading, Fading effects Due to Doppler Spread-Fast fading, slow fading, Statistical Models for multipath Fading Channels- Clarke"s model for flat fading, spectral shape due to Doppler spread in Clarke"s model,



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Simulation of Clarke and Gans Fading Model, Level crossing and fading statistics, Two-ray Rayleigh Fading Model.

UNIT-IV:

Equalization and Diversity

Introduction, Fundamentals of Equalization, Training a Generic Adaptive Equalizer, Equalizers in a communication Receiver, Linear Equalizers, Non-linear Equalization-Decision Feedback Equalization (DFE), Maximum Likelihood Sequence Estimation (MLSE) Equalizer, Algorithms for adaptive equalization-Zero Forcing Algorithm, Least Mean Square Algorithm, Recursive least squares algorithm. Diversity -Derivation of selection Diversity improvement, Derivation of Maximal Ratio Combining improvement, Practical Space Diversity Consideration-Selection Diversity, Feedback or Scanning Diversity, Maximal Ratio Combining, Equal Gain Combining, Polarization Diversity, Frequency Diversity, Time Diversity, RAKE Receiver.

UNIT-V:

Wireless Networks

Introduction to wireless Networks, Advantages and disadvantages of Wireless Local Area Networks, WLAN Topologies, WLAN Standard IEEE 802.11, IEEE 802.11 Medium Access Control, Comparison of IEEE 802.11 a,b,g and n standards, IEEE 802.16 and its enhancements, Wireless PANs, HiperLan, WLL.

TEXTBOOKS:

1. Wireless-Communications,Principles,Practice–Theodore,S.Rappaport,2ndEd.,2002,PHI.
2. Wireless Communications-Andrea Goldsmith,2005 Cambridge University Press.
3. Mobile Cellular Communication– Gottapu Sasi Bhushana Rao, Pearson Education,2012.

REFERENCE BOOKS:

1. Principles of Wireless Networks–KavehPahLavenandP.KrishnaMurthy,2002, PE
2. Wireless Digital Communications–Kamilo Feher, 1999, PHI.
3. Wireless Communication and Networking–WilliamStallings,2003,PHI.
4. Wireless Communication–Upen Dalal, Oxford Univ.Press
5. Wireless Communications and Networking–Vijay K.Gary, Elsevier.

Course Outcomes:

At the end of this course, students can be able to

1. Understand Cellular communication concepts
2. Study them mobile radio propagation
3. Study the wireless network different type of MAC protocols



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1203	L	T	P	C
		3	0	0	3
CMOS ANALOG AND DIGITAL IC DESIGN (ELECTIVE-III)					

UNIT-I :

MOS Devices and Modeling : The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.

MOS Design: Pseudo NMOS Logic – Inverter, Inverter threshold voltage, Output high voltage, Output Low voltage, Gain at gate threshold voltage, Transient response, Rise time, Fall time, Pseudo NMOS logic gates, Transistor equivalency, CMOS Inverter logic.

UNIT-II:

Combinational MOS Logic Circuits: MOS logic circuits with NMOS loads, Primitive CMOS logic gates –NOR & NAND gate, Complex Logic circuits design – Realizing Boolean expressions using NMOS gates and CMOS gates , AOI and OIA gates, CMOS full adder, CMOS transmission gates, Designing with Transmission gates.

Sequential MOS Logic Circuits: Behaviour of bistable elements, SR Latch, Clocked latch and flip flop circuits, CMOS D latch and edge triggered flip-flop.

UNIT-III:

Dynamic Logic Circuits: Basic principle, Voltage Bootstrapping, Synchronous dynamic pass transistor circuits, Dynamic CMOS transmission gate logic, High performance Dynamic CMOS circuits.

Semiconductor Memories: Types, RAM array organization, DRAM – Types, Operation, Leakage currents in DRAM cell and refresh operation, SRAM operation Leakage currents in SRAM cells, Flash Memory- NOR flash and NAND flash.

UNIT-IV:

Analog CMOS Sub-Circuits: MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascade current Mirror and Wilson Current Mirror, Current and Voltage References, Band gap Reference.

UNIT-V:

CMOS Amplifiers: Inverters, Differential Amplifiers, Cascade Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.

CMOS Operational Amplifiers: Design of CMOS Op Amps, Compensation of Op Amps, Design of Two-Stage Op Amps, Power- Supply Rejection Ratio of Two-Stage Op Amps, Cascade Op Amps, Measurement Techniques of OP Amp.



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TEXT BOOKS:

1. Digital Integrated Circuit Design– Ken Martin, Oxford University Press, 2011.
2. CMOS Digital Integrated Circuits Analysis and Design –Sung-Mo Kang, Yusuf Leblebici, TMH, 3rd Ed., 2011.
3. CMOS Analog Circuit Design –Philip E.Allenand Douglas R .Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.
4. Analysis and Design of Analog Integrated Circuits-Paul R.Gray,Paul J.Hurst,S.Lewisand R.G.Meyer,Wiley India, Fifth Edition, 2010
5. Analog Integrated Circuit Design-David A.Johns, Ken Martin, Wiley Student Edn, 2016.
6. Design of Analog CMOS Integrated Circuits-Behzad Razavi, TMH Edition.
7. CMOS: Circuit Design, Layout and Simulation-Baker, Liand Boyce, PHI.
8. Digital Integrated Circuits –A Design Perspective, Jan M.Rabaey, Anantha Chandrakasan, Borivoje Nikolic, 2nd Ed., PHI.

Course Outcomes:

At the end of this course, students will be able to

1. Analyze, design, optimize and simulate analog and digital circuits using CMOS constrained by the design metrics.
2. Connect the individual gates to form the building blocks of a system.
3. Use EDA tools like Cadence, Mentor Graphics and other open source software tools like Ng spice.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1204	L	T	P	C
		3	0	0	3
ADVANCED COMPUTER ARCHITECTURE (ELECTIVE-III)					

UNIT-I:

Fundamentals of Computer Design:

Fundamentals of Computer design, Changing faces of computing and task of computer designer, Technology trends, Cost price and their trends, measuring and reporting performance, Quantitative principles of computer design, Amdahl's law. Instruction set principles and examples- Introduction, classifying instruction set- memory addressing-type and size of operands, Operations in the instruction set.

UNIT-II: Pipelines:

Introduction, basic RISC instruction set, Simple implementation of RISC instruction set, Classic five stage pipe lined RISC processor, Basic performance issues in pipelining, Pipeline hazards, Reducing pipeline branch penalties.

Memory Hierarchy Design: Introduction, review of ABC of cache, Cache performance, Reducing cache miss penalty, Virtual memory.

UNIT-III:

Instruction Level Parallelism (ILP)-The Hardware Approach: Instruction-Level parallelism, Dynamic scheduling, Dynamic scheduling using Tomasulo's approach, Branch prediction, High performance instruction delivery- Hardware based speculation.

ILP Software Approach: Basic compiler level techniques, Static branch prediction, VLIW approach, Exploiting ILP, Parallelism at compile time, Cross cutting issues - Hardware verses Software.

UNIT-IV: Multi Processors and Thread Level Parallelism:

Multi Processors and Thread level Parallelism- Introduction, Characteristics of application domain, Systematic shared memory architecture, Distributed shared – Memory architecture, Synchronization.

UNIT-V: Inter Connection and Networks:

Introduction, Interconnection network media, Practical issues in interconnecting networks, Examples of inter connection, Cluster, Designing of clusters.

Intel Architecture: Intel IA-64 IL Pin embedded and mobile markets Fallacies and pit falls.



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TEXT BOOKS:

1. John L. Hennessy, David A. Patterson - Computer Architecture: A Quantitative Approach, 3rd Edition, an Imprint of Elsevier.

REFERENCE BOOKS:

1. John P. Shen and Miikko H.Lipasti-, Modern Processor Design: Fundamentals of Super Scalar Processors
2. Computer Architecture and Parallel Processing- KaiHwang, Faye A.Brigs.,MC GrawHill.
3. Advanced Computer Architecture-A Design Space Approach, Dezso Sima, Terence Fountain, Peter Kacsuk, Pearson Ed.

Course Outcomes:

At the end of this course, students will be able to

1. Understand parallelism and pipelining concepts, the design aspects and challenges.
2. Evaluate the issues in vector and array processors.
3. Study and analyze the high performance scalable multi threaded and multi processor systems.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1205	L	T	P	C
		3	0	0	3
SOFT COMPUTING TECHNIQUES (ELECTIVE -III)					

UNIT –I:

Introduction:

Approaches to intelligent control, Architecture for intelligent control, Symbolic reasoning system, Rule- based systems, the AI approach, Knowledge representation - Expert systems.

UNIT –II:

Artificial Neural Networks:

Concept of Artificial Neural Networks and its basic mathematical model, Mc Culloch-Pitts neuron model, simple perceptron, Adaline and Madaline, Feed-forward Multi layer Perceptron, Learning and Training the neural network, Data Processing: Scaling, Fourier transformation, principal-component analysis and wavelet transformations, Hopfield network, Self-organizing network and Recurrent network, Neural Network based controller.

UNIT –III:

Fuzzy Logic System:

Introduction to crisp sets and fuzzy sets, basic fuzzy set operation and approximate reasoning, Introduction to fuzzy logic modeling and control, Fuzzification, inferencing and defuzzification, Fuzzy knowledge and rule bases, Fuzzy modeling and control schemes for nonlinear systems, Self-organizing fuzzy logic control, Fuzzy logic control for nonlinear time delay system.

UNIT –IV:

Genetic Algorithm:

Basic concept of Genetic algorithm and detail algorithmic steps, Adjustment of free parameters, Solution of typical control problems using genetic algorithm, Concept on some other search techniques like Tabu search and an D-colony search techniques for solving optimization problems.

UNIT –V:

Applications:

GA application to power system optimisation problem, Case studies: Identification and control of linear and nonlinear dynamic systems using MATLAB-Neural Network toolbox, Stability analysis of Neural- Network interconnection systems, Implementation of fuzzy logic controller using MATLAB fuzzy-logic toolbox, Stability analysis of fuzzy control systems.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

TEXT BOOKS:

1. Introduction to Artificial Neural Systems-Jacek.M.Zurada, Jaico Publishing House, 1999.
2. Neural Networks and Fuzzy Systems-Kosko,B.,Prentice-Hall ofIndiaPvt.Ltd.,1994.

REFERENCE BOOKS:

1. Fuzzy Sets, Uncertainty and Information –Klir G.J.&Folger T.A.,Prentice-Hall of India Pvt.Ltd., 1993.
2. Fuzzy Set Theory and Its Applications-Zimmerman H.J.Kluwer Academic Publishers,1994.
3. Introduction to Fuzzy Control-Driankov, Hellendroon, Narosa Publishers.
4. Artificial Neural Networks-Dr.B.Yagananarayana,1999,PHI, New Delhi.
5. Elements of Artificial Neural Networks-Kishan Mehrotra,Chelkuri K.Mohan,Sanjay Ranka, Penram International.
6. Artificial Neural Network–SimonHaykin,2ndEd.,PearsonEducation.
7. Introduction Neural Networks Using MATLAB 6.0 - S.N. Shivanandam, S. Sumati, S. N. Deepa,1/e, TMH, New Delhi.

Course Outcomes

At the end of this course the student can able to:

1. Understand the basic concepts of Artificial neural network systems.
2. Understand the Mc Culloch-Pitts neuron model,simple and multi layer Perception, Adeline and Madeline concepts.
3. Data processing, Hop field and self-organizing network
4. Difference between crisp sets to fuzzy sets, fuzzy models, fuzzification, inference,
5. Member ship functions, rule based approaches and defuzzification.
6. Self–organizing fuzzy logic control, nonlinear time delay systems.
7. Understand the concept of Genetic Algorithm steps. Tabu, an D-colony search techniques for solving optimization problems.
8. GA applications to power system optimization problems ,identification and control of linear and non linear dynamic systems using MATLAB-Neural network toolbox.
9. Know the application and importance stability analysis



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1206	L	T	P	C
		3	0	0	3
DIGITAL SIGNAL PROCESSORS AND ARCHITECTURES (ELECTIVE -IV)					

Course Objectives:

- 1) To recall the digital transform techniques (Fourier and z-domain).
- 2) To introduce architectural features of programmable DSP Processors of Texas Instruments (TI's) and Analog Devices (AD's).
- 3) To give practical examples of DSP Processor architectures for better understanding.
- 4) To develop the programming knowledge using Instruction set of DSP Processors.
- 5) To understand interfacing techniques to memory and I/O devices.

UNIT –I:

Introduction to Digital Signal Processing:

Introduction, A Digital signal-processing system, The sampling process, Discrete time sequences. Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), Linear time-invariant systems, Digital filters, Decimation and interpolation.

Computational Accuracy in DSP Implementations:

Number formats for signals and coefficients in DSP systems, Dynamic Range and Precision, Sources of error in DSP implementations, A/D Conversion errors, DSP Computational errors, D/A Conversion Errors, Compensating filter.

UNIT –II:

Architectures for Programmable DSP Devices:

Basic Architectural features, DSP Computational Building Blocks, Bus Architecture and Memory, Data Addressing Capabilities, Address Generation UNIT, Programmability and Program Execution, Speed Issues, Features for External interfacing.

UNIT-III:

Programmable Digital Signal Processors:

Commercial Digital signal-processing Devices, Data Addressing modes of TMS320C54XX DSPs, Data Addressing modes of TMS320C54XX Processors, Memory space of TMS320C54XX Processors, Program Control, TMS320C54XX instructions and Programming, On-Chip Peripherals, Interrupts of TMS320C54XX processors, Pipeline operation of TMS320C54XX Processors.

UNIT –IV:

Analog Devices Family of DSP Devices:

Analog Devices Family of DSP Devices – ALU and MAC block diagram, Shifter Instruction, Base Architecture of ADSP 2100, ADSP-2181 high performance Processor. Introduction to Blackfin Processor -The Blackfin Processor, Introduction to Micro Signal Architecture, Overview of Hardware Processing Units and Register files, Address Arithmetic Unit, Control Unit, Bus Architecture and Memory, Basic Peripherals.



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UNIT –V:

Interfacing Memory and I/O Peripherals to Programmable DSP Devices:

Memory space organization, External bus interfacing signals, Memory interface, Parallel I/O interface, Programmed I/O, Interrupts and I/O, Direct memory access (DMA).

TEXTBOOKS:

1. Digital Signal Processing–Avtar Singh and S.Srinivasan,Thomson Publications, 2004.
2. A Practical Approach to Digital Signal Processing - K Padmanabhan, R. Vijayarajeswaran, Ananthi. S, New Age International, 2006/2009
3. Embedded Signal Processing with the Micro Signal Architecture Publisher: Woon-SengGan, SenM.Kuo, Wiley-IEEE Press, 2007

REFERENCE BOOKS:

1. Digital Signal Processors, Architecture, Programming and Applications – B. Venkataramani and M. Bhaskar, 2002, TMH.
2. Digital Signal Processing –Jonatham Stein, 2005,JohnWiley.
3. DSP Processor Fundamentals, Architectures & Features–Lapsleyetal.2000,S.Chand&Co.
4. Digital Signal Processing Applications Using the ADSP-2100FamilybyTheApplications Engineering Staff of Analog Devices, DSP Division, Edited by Amy Mar, PHI
5. The Scientist and Engineer's Guide to Digital Signal Processing by Steven W. Smith, Ph.D., California Technical Publishing, ISBN 0-9660176-3-3, 1997
6. Embedded Media Processing by David J. Katz and Rick Gentile of Analog Devices, Newnes , ISBN 0750679123, 2005

Course Outcomes:

At the end of this course, students will be able to

- 1) Understand the basics concepts of Digital Signal Processing (DSP) and transforms.
- 2) To distinguish between the architectural features of General purpose processors and Programmable DSP processors.
- 3) Understand the architectures of TMS320C54xx devices.
- 4) Understand the architectures of ADSP 2100DSP devices and Blackfin Processor.
- 5) Interface various devices to DSP Processors.
- 6) Able to write simple assembly language programs using instruction set of TMS320C54xx.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1207	L	T	P	C
		3	0	0	3
ELECTROMAGNETIC INTERFERENCE AND ELECTROMAGNETIC COMPATIBILITY (EMI / EMC) (ELECTIVE-IV)					

Course objectives:

1. To introduce enough knowledge regarding the Electromagnetic interference/ Electromagnetic compatibility, Its practical experiences and concerns, and various sources both the natural and Nuclear sources of EMI.
2. To know the practical experiences due to EMI such as mains power supply, switches and relays etc and Analyze EM Propagation and Crosstalk
3. To know various methods of the measurements radiated and conducted interference in open area test sites and in chambers.
4. To Learn about the various methods of minimizing the EMI.
5. To know the National/International EMC Standards.

UNIT-I:

Introduction, Natural and Nuclear Sources of EMI / EMC: Electromagnetic environment, History, Concepts, Practical experiences and concerns, frequency spectrum conservations, An overview of EMI / EMC, Natural and Nuclear sources of EMI.

UNIT-II:

EMI from Apparatus, Circuits and Open Area Test Sites: Electromagnetic emissions, Noise from relays and switches, Non-linearities in circuits, passive inter modulation, Cross talk in transmission lines, Transients in power supply lines, Electromagnetic interference (EMI), Open area test sites and measurements.

UNIT-III:

Radiated and Conducted Interference Measurements and ESD: Anechoic chamber, TEM cell, GH TEM Cell, Characterization of conduction currents / voltages, Conducted EM noise on power lines, Conducted EMI from equipment, Immunity to conducted EMI detectors and measurements, ESD, Electrical fast transients / bursts, Electrical surges.

UNIT-IV:

Grounding, Shielding, Bonding and EMI filters: Principles and types of grounding, Shielding and bonding, Characterization of filters, Power lines filter design.

UNIT-V:

Cables, Connectors, Components and EMC Standards:

EMI suppression cables, EMC connectors, EMC gaskets, Isolation transformers, optoisolators, National / International EMC standards.



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TEXT BOOKS:

1. Engineering Electromagnetic Compatibility - Dr. V.P. Kodali, IEEE Publication, Printed in India by S. Chand & Co. Ltd., New Delhi, 2000.
2. Electromagnetic Interference and Compatibility IMPACT series, IIT-Delhi, Modules 1-9

REFERENCE BOOKS:

1. Introduction to Electromagnetic Compatibility-Ny, John Wiley, 1992, by C.R.Pal.

Course outcomes:

At the end of this course the student can able to:

1. Understand the electromagnetic environment the definitions of EMI and EMC, history of EMI some examples of practical experiences due to EMI such as mains power supply, switches and relays etc.
2. Understand the celestial electromagnetic noise the occurrence of lightning discharge and their effects, the charge accumulation and discharge in an electrostatic discharge, model ESD wave form, the various cases of nuclear explosion and the transients.
3. Understand the methods to measure RE and RS in the open area test sites
4. Understand the measurement facilities and procedures using an echoic chamber, TEM cell, reverberating chamber GTEM cell.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1208	L	T	P	C
		3	0	0	3
OBJECT-ORIENTED PROGRAMMING (ELECTIVE IV)					

OBJECTIVES:

The main objectives of this course are given below:

- Its main objective is to teach the basic concepts and techniques and java program structure which form the object oriented programming paradigm

UNIT- I:

Objective: Focus on object oriented concepts and java program structure and its installation

Introduction to OOP

Introduction, Need of Object Oriented Programming, Principles of Object Oriented Languages, Procedural languages Vs OOP, Applications of OOP, History of JAVA, Java Virtual Machine, Java Features, Installation of JDK1.6

UNIT- II:

Objective: Comprehension of java programming constructs, control structures in Java

Programming Constructs

Variables, Primitive Data types, Identifiers- Naming Conventions, Keywords, Literals, Operators- Binary, Unary and ternary, Expressions, Precedence rules and Associativity, Primitive Type Conversion and Casting, Flow of control-Branching, Conditional, loops.,

Classes and Objects- classes, Objects, Creating Objects, Methods, constructors-Constructor overloading, Garbage collector, Class variable and Methods-Static keyword, this keyword, Arrays, Command line arguments

UNIT-III:

Objective: Implementing Object oriented constructs such as various class hierarchies, interfaces and exception handling

Inheritance: Types of Inheritance, Deriving classes using extends keyword, Method overloading, super keyword, final keyword, Abstract class

Interfaces, Packages and Enumeration: Interface-Extending interface, Interface Vs Abstract classes, Packages-Creating packages , using Packages, Access protection, java.lang package

Exceptions & Assertions-Introduction, Exception handling techniques-try...catch, throw, throws, finally block, user defined exception, Assertions



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UNIT -IV:

Objective: Understanding of Thread concepts and I/O in Java

Multi Threading: java. lang. Thread, The main Thread, Creation of new threads, Thread priority, Multithreading, Synchronization, suspending and Resuming threads, Communication between Threads

Input/Output: reading and writing data, java.io package

UNIT -V:

Objective: Being able to build dynamic user interfaces using applets and Event handling in java Applets-Applet class, Applet structure, An Example Applet Program, Applet Life Cycle, paint(),update() and repaint()

Event Handling -Introduction, Event Delegation Model, java.awt.event Description, Event Listeners, Adapter classes, Inner classes

Understanding of various components of Java AWT and Swing and writing code snippets using them

Abstract Window Toolkit

Why

AWT?,java.awtpackage,ComponentsandContainers,Button,Label,Checkbox,Radiobuttons, List boxes, Choice boxes, Text field and Text area, container classes, Layouts, Menu, Scroll bar

Swing: Introduction, JFrame, JApplet, JPanel,Components in swings, Layout Managers, JListand JScroll Pane, Split Pane, JTabbedPane, Dialog Box

TEXT BOOKS:

1. The Complete Reference Java,8ed,HerbertSchildt, TMH
2. Programming in JAVA, Sachin Malhotra, Saurabh choudhary, Oxford.
3. JAVA for Beginners,4e, Joyce Farrell ,Ankit R.Bhavsar, Cengage Learning.
4. Object oriented programming with JAVA, Essentials and Applications, RajKumar Bhuyya, Selvi, Chu TMH
5. Introduction to Java Programming, 7thed,YDaniel Liang,Pearson

REFERENCE BOOKS:

1. JAVA Programming, K.Rajkumar.Pearson
2. Core JAVA,Black Book,NageswaraRao,Wiley, Dream Tech
3. Core JAVA for Beginners,Rashmi KantaDas,Vikas.
4. Object Oriented Programming through JAVA, P Radha Krishna, University Press.

OUTCOMES:

At the end of this course the student can able to:

1. The model of object oriented programming :abstract data types, encapsulation, inheritance and polymorphism
2. Fundamental features of an object oriented language like Java: object classes and interfaces, exceptions and libraries of object collections
3. How to take the statement of a business problem and from this determine suitable logic for solving the problem; then be able to proceed to code that logic as a program written in Java.
4. How to test, document and prepare a professional looking package for each business project using java doc.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1209	L	T	P	C
		0	0	4	2
ADVANCED COMMUNICATIONS LAB					

Notice:

- A. **Minimum of 10 Experiments have to be conducted.**
- B. **All Experiments may be Simulated using MATLAB and to be verified using related training kits.**
 1. Measurement of Bit Error Rate using Binary Data
 2. Verification of minimum distance in Hamming code
 3. Determination of outpour of Convolutional Encoder for a given sequence
 4. Determination of outpour of Convolutional Decoder for a given sequence
 5. Efficiency of DS Spread-Spectrum Technique
 6. Effect of Sampling and Quantization of Digital Image
 7. Verification of Various Transforms(FT/DCT/Walsh/Hadamard)on a given Image (Finding Transform and Inverse Transform)
 8. Point, Line and Edge detection techniques using derivative operators.
 9. Implementation of FIR filter using DSP Trainer Kit (C-Code/Assembly code)
 10. Implementation of IIR filter using DSP Trainer Kit(C-Code/Assembly code)
 11. Determination of Losses in Optical Fiber
 12. Observing the Waveforms at various test points of a mobile phone using Mobile Phone Trainer
 13. Study of Direct Sequence Spread Spectrum Modulation & Demodulation using CDMA-DSS-BER Trainer
 14. Study of ISDN Training System with Protocol Analyzer
 15. Characteristics of LASER Diode.

Course Outcomes:

At the end of this course, students will be able to

1. Identify the different types of network devices and their functions within a network.
2. Understand and build the skills of sub-netting and routing mechanisms.
3. Understand basic protocols of computer networks, and how they can be used to assist in network design and implementation.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1210	L	T	P	
		0	0	4	
ADVANCED DIGITAL IMAGE AND VIDEO PROCESSING LAB					

List of Experiments:

1. Perform basic operations on images like addition, subtraction etc.
2. Plot the histogram of an image and perform histogram equalization
3. Implement segmentation algorithms
4. Perform video enhancement
5. Perform video segmentation
6. Perform image compression using lossy technique
7. Perform image compression using loss less technique
8. Perform image restoration
9. Convert a colour model into another
10. Calculate boundary features of an image
11. Calculate regional features of an image
12. Detect an object in an image/video using template matching/Bayes classifier

Course Outcomes:

At the end of this course, students will be able to

1. Perform and analyze image and video enhancement and restoration
2. Perform and analyze image and video segmentation and compression
3. Work and process viz., detection, extraction on the image/video



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1211	L	T	P	C
		0	0	4	2
MINIPROJECT					

Syllabus Contents

The students are required to search/gather the material/information on a specific a topic comprehend it and present / discuss in the class.

Course Outcomes

At the end of this course, students will be able to

1. Understand of contemporary/emerging technology for various processes and systems.
2. Share knowledge effectively in oral and written.
3. Inform and formulate documents



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II Year – III Semester	Course Code: MT24DE2101	L	T	P	C
		3	0	0	3
DETECTION & ESTIMATION THEORY					

UNIT –I:

Random Processes: Discrete Linear Models, Markov Sequences and Processes, Point Processes, and Gaussian Processes.

UNIT –II:

Detection Theory: Basic Detection Problem, Maximum A posteriori Decision Rule, Minimum Probability of Error Classifier, Bayes Decision Rule, Multiple-Class Problem (Bayes)-minimum probability error with and without equal a priori probabilities, Neyman-Pearson Classifier, General Calculation of Probability of Error, General Gaussian Problem, Composite Hypotheses.

UNIT –III:

Linear Minimum Mean-Square Error Filtering: Linear Minimum Mean Squared Error Estimators, Nonlinear Minimum Mean Squared Error Estimators. Innovations, Digital Wiener Filters with Stored Data, Real-time Digital Wiener Filters, Kalman Filters.

UNIT –IV:

Statistics: Measurements, Nonparametric Estimators of Probability Distribution and Density Functions, Point Estimators of Parameters, Measures of the Quality of Estimators, Introduction to Interval Estimates, Distribution of Estimators, Tests of Hypotheses, Simple Linear Regression, Multiple Linear Regression.

UNIT –V:

Estimating the Parameters of Random Processes from Data: Tests for Stationarity and Ergodicity, Model-free Estimation, Model-based Estimation of Autocorrelation Functions, Power Spectral Density Functions.

TEXTBOOKS:

1. Random Signals: Detection, Estimation and Data Analysis - K. Sam Shanmugan & A.M. Breipohl, Wiley India Pvt. Ltd, 2011.
2. Random Processes: Filtering, Estimation and Detection –Lonnie C.Ludeman,Wiley India Pvt.Ltd., 2010.



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REFERENCE BOOKS:

1. Fundamentals of Statistical Signal Processing: Volume I Estimation Theory– Steven.M.Kay, Prentice Hall, USA, 1998.
2. Fundamentals of Statistical Signal Processing :Volume I Detection Theory– Steven.M.Kay,Prentice Hall, USA, 1998.
3. Introduction to Statistical Signal Processing with Applications - Srinath, Rajasekaran, Viswanathan, 2003, PHI.
4. Statistical Signal Processing :Detection,Estimation and Time Series Analysis– LouisL.Scharf, 1991, Addison Wesley.
5. Detection, Estimation and Modulation Theory: Part – I – Harry L. Van Trees, 2001, John Wiley & Sons, USA.
6. Signal Processing: Discrete Spectral Analysis – Detection & Estimation – Mischa Schwartz, Leonard Shaw, 1975, McGraw Hill.

Course Outcomes:

At the end of this course,students will be able to

1. Understand the mathematical background of signal detection and estimation
2. Use classical and Bayesian approaches to formulate and solve problems for signal detection and parameter estimation from noisy signals.
3. Derive and apply filtering methods for parameter estimation.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II Year – III Semester	Course Code: MT24DE2102	L	T	P	C
		3	0	0	3
ADVANCED DIGITAL SIGNAL PROCESSING (ELECTIVE IV)					

UNIT-I

Review of DFT,FFT,IIR Filters and FIR Filters:

Multi Rate Signal Processing: Introduction, Decimation by a factor D, Interpolation by a factor I, Sampling rate conversion by a rational factor I/D, Multistage Implementation of Sampling Rate Conversion, Filter design & Implementation for sampling rate conversion.

UNIT-II:

Applications of Multi Rate Signal Processing: Design of Phase Shifters, Interfacing of Digital Systems with Different Sampling Rates, Implementation of Narrow Band Low Pass Filters, Implementation of Digital Filter Banks, Sub-band Coding of Speech Signals, Quadrature Mirror Filters, Trans-multiplexers, Over Sampling A/D and D/A Conversion.

UNIT-III:

Non-Parametric Methods of Power Spectral Estimation: Estimation of spectra from finite duration observation of signals, Non-parametric Methods: Bartlett, Welch & Blackman-Tukey methods, Comparison of all Non-Parametric methods

UNIT-IV:

Implementation of Digital Filters: Introduction to filter structures (IIR & FIR), Frequency sampling structures of FIR, Lattice structures, Forward prediction error, Backward prediction error, Reflection coefficients for lattice realization, Implementation of lattice structures for IIR filters, Advantages of lattice structures.

UNIT-V:

Parametric Methods of Power Spectrum Estimation: Autocorrelation & Its Properties, Relation between auto correlation & model parameters, AR Models - Yule-Walker & Burg Methods, MA & ARMA models for power spectrum estimation, Finite word length effect in IIR digital Filters – Finite word-length effects in FFT algorithms.

TEXT BOOKS:

1. Digital Signal Processing: Principles, Algorithms & Applications - J.G.Proakis & D. G. Manolakis, 4th Ed., PHI.
2. Discrete Time Signal Processing-Alan VO ppenheim &R.WSchaffer, PHI.
3. DSP–A Practical Approach–Emmanuel C.Ifeacher,Barrie.W.Jervis,2Ed.,PearsonEducation.



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REFERENCEBOOKS:

1. Modern Spectral Estimation: Theory&Application–S.M.Kay,1988, PHI.
2. Multi Rate Systems and Filter Banks–P.P.Vaidyanathan–Pearson Education.
3. Digital Signal Processing–S.Salivahanan,A.Vallavaraj,C.Gnanapriya,2000,TMH
4. Digital Spectral Analysis–Jr. Marple

Course Outcomes:

At the end of this course, students will be able to

1. To understand theory of different filters and algorithms
2. To understand theory of multi rate DSP, solve numerical problems and write algorithms
3. To understand theory of prediction and solution of normal equations
4. To know applications of DSP at block level



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II Year – III Semester	Course Code: MT24DE2103	L	T	P	C
		3	0	0	3
CODING THEORY AND APPLICATIONS (ELECTIVE IV)					

UNIT –I:

Coding for Reliable Digital Transmission and Storage: Mathematical model of Information, A Logarithmic Measure of Information, Average and Mutual Information and Entropy, Types of Errors, Error Control Strategies.

Linear Block Codes: Introduction to Linear Block Codes, Syndrome and Error Detection, Minimum Distance of a Block code, Error-Detecting and Error-correcting Capabilities of a Block code, Standard array and Syndrome Decoding, Probability of an undetected error for Linear Codes over a BSC, Hamming Codes. Applications of Block codes for Error control in data storage system

UNIT –II:

Cyclic Codes: Description, Generator and Parity-check Matrices, Encoding, Syndrome Computation and Error Detection, Decoding, Cyclic Hamming Codes, Shortened cyclic codes, Error-trapping decoding for cyclic codes, Majority logic decoding for cyclic codes.

UNIT –III:

Convolutional Codes: Encoding of Convolutional Codes, Structural and Distance Properties, maximum likelihood decoding, Sequential decoding, Majority- logic decoding of Convolution codes. Application of Viterbi Decoding and Sequential Decoding, Applications of Convolutional codes in ARQ system.

UNIT –IV:

Burst –Error-Correcting Codes: Decoding of Single-Burst error Correcting Cyclic codes, Single- Burst-Error-Correcting Cyclic codes, Burst-Error-Correcting Convolutional Codes, Bounds on Burst Error-Correcting Capability, Interleaved Cyclic and Convolutional Codes, Phased-Burst –Error- Correcting Cyclic and Convolutional codes.

UNIT-V:

BCH – Codes: BCH code- Definition, Minimum distance and BCH Bounds, Decoding Procedure for BCH Codes- Syndrome Computation and Iterative Algorithms, Error Location Polynomials and Numbers for single and double error correction

TEXTBOOKS:

1. Error Control Coding- Fundamentals and Applications–ShuL in, Daniel J.Costello,Jr, Prentice Hall, Inc.
2. Error Correcting Coding Theory-ManYoungRhee-1989,McGraw-Hill Publishing.



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REFERENCE BOOKS:

1. Digital Communications –Fundamental and Application-BernardSklar,PE.
2. Digital Communications –John G.Proakis, 5th Ed.,2008,TMH.
3. Introduction to Error Control Codes-Salvatore Gravano-oxford
4. Error Correction Coding–Mathematical Methods and Algorithms– ToddK.Moon, 2006, Wiley India.
5. Information Theory,Coding and Cryptography–RanjanBose,2nd Ed,2009,TMH.

Course Outcomes:

On completion of this course student twill be able to

1. Learning the measurement of information and errors.
2. Obtain knowledge in designing Linear Block Codes and Cyclic codes.
3. Construct tree and trellies diagrams for convolution codes
4. Design the Turbo codes and Space time codes and also their applications



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II Year – III Semester	Course Code: MT24DE2104	L	T	P	C
		3	0	0	3
BUSINESS ANALYTICS (OPEN ELECTIVE)					

UNIT-I:

Business analytics: Overview of Business analytics, Scope of Business analytics, Business Analytics Process, Relationship of Business Analytics Process and organisation, competitive advantages of Business Analytics.

Statistical Tools: Statistical Notation, Descriptive Statistical methods,

Review of probability distribution and data modelling, sampling and estimation methods overview.

UNIT-II:

Trendiness and Regression Analysis: Modelling Relationships and Trends in Data, simple Linear Regression. Important Resources, Business Analytics Personnel, Data and models for Business analytics, problem solving, Visualizing and Exploring Data, Business Analytics Technology

UNIT-III:

Organization Structures of Business analytics, Team management, Management Issues, Designing Information Policy, Outsourcing, Ensuring Data Quality, Measuring contribution of Business analytics, Managing Changes. Descriptive Analytics, predictive analytics, predictive Modelling, Predictive analytics analysis, Data Mining, Data Mining Methodologies, Prescriptive analytics and its step in the business analytics Process, Prescriptive Modelling, nonlinear Optimization.

UNIT-IV:

Forecasting Techniques: Qualitative and Judgmental Forecasting, Statistical Forecasting Models, Forecasting Models for Stationary Time Series, Forecasting Models for Time Series with a Linear Trend, Forecasting Time Series with Seasonality, Regression Forecasting with Casual Variables, Selecting Appropriate Forecasting Models.

Monte Carlo Simulation and Risk Analysis: Monte Carle Simulation

Using Analytic Solver Platform, New-Product Development Model, Newsvendor Model, Overbooking Model, Cash Budget Model.

UNIT-V:

Decision Analysis: Formulating Decision Problems, Decision Strategies with the without Outcome Probabilities, Decision Trees, The Value of Information, Utility and Decision Making.

UNIT-VI:

Recent Trends in : Embedded and collaborative business intelligence, Visual data recovery, Data Storytelling and Data journalism



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Reference:

1. Business analytics Principles, Concepts, and Applications by Marc J. Schniederjans, Dara G. Schniederjans, Christopher M. Starkey, Pearson FT Press.
2. Business Analytics by James Evans, persons Education.

COURSE OUTCOMES

1. Students will demonstrate knowledge of data analytics.
2. Students will demonstrate the ability of think critically in making decisions based on data and deep analytics.
3. Students will demonstrate the ability to use technical skills in predictive and prescriptive modeling to support business decision-making.
4. Students will demonstrate the ability to translate data in to clear, action able in sights



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II Year – III Semester	Course Code: MT24DE2105	L	T	P	C
		3	0	0	3
INDUSTRIAL SAFETY (OPEN ELECTIVES)					

UNIT-I :

Industrial safety: Accident, causes, types, results and control, mechanical and electrical hazards, types, causes and preventive steps/procedure, describe salient points of factories act 1948 for health and safety, wash rooms, drinking water layouts, light, cleanliness, fire, guarding, pressure vessels, etc, Safety color codes. Fire prevention and firefighting, equipment and methods.

UNIT-II :

Fundamentals of maintenance engineering: Definition and aim of maintenance engineering, Primary and secondary functions and responsibility of maintenance department, Types of maintenance, Types and applications of tools used for maintenance, Maintenance cost & its relation with replacement economy, Service life of equipment.

UNIT-III:

Wear and Corrosion and their prevention: Wear- types, causes, effects, wear reduction methods, lubricants-types and applications, Lubrication methods, general sketch, working and applications, i. Screw down grease cup, ii. Pressure grease gun, iii. Splash lubrication, iv. Gravity lubrication, v. Wick feed lubrication vi. Side feed lubrication, vii. Ring lubrication, Definition, principle and factors affecting the corrosion. Types of corrosion, corrosion prevention methods.

UNIT-IV :

Fault tracing: Fault tracing-concept and importance, decision tree concept, need and applications, sequence of fault finding activities, show as decision tree, draw decision tree for problems in machine tools, hydraulic, pneumatic, automotive, thermal and electrical equipment's like, I. Any one machine tool, ii. Pump iii. Air compressor, iv. Internal combustion engine, v. Boiler, vi. Electrical motors, Types of faults in machine tools and their general causes.

UNIT-V :

Periodic and preventive maintenance : Periodic inspection-concept and need, degreasing, cleaning and repairing schemes, overhauling of mechanical components, overhauling of electrical motor, common troubles and remedies of electric motor, repair complexities and its use, definition, need, steps and advantages of preventive maintenance. Steps/procedure for periodic and preventive maintenance of:

I. Machine tools, ii. Pumps, iii. Air compressors, iv. Diesel generating (DG) sets, Program and schedule of preventive maintenance of mechanical and electrical equipment, advantages of preventive maintenance. Repair cycle concept and importance

Reference:

1. Maintenance Engineering Handbook, Higgins & Morrow, Da Information Services.
2. Maintenance Engineering, H.P.Garg, S.Chandand Company.
3. Pump-hydraulic Compressors, Audels, McGraw Hill Publication.
4. Foundation Engineering Handbook, Winterkorn, Hans, Chapman & Hall London



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II Year – III Semester	Course Code: MT24DE2106	L	T	P	C
		3	0	0	3
OPERATIONS RESEARCH (OPEN ELECTIVES)					

UNIT-I :

Optimization Techniques, Model Formulation, models, General L.R Formulation, Simplex Techniques, Sensitivity Analysis, Inventory Control Models

UNIT-II :

Formulation of a LPP-Graphical solution revised simplex method-duality theory-dual simplex method - sensitivity analysis - parametric programming

UNIT-III :

Non linear programming problem -Kuhn-Tucker conditions min cost flow problem –max flow problem- CPM/PERT

UNIT-IV :

Scheduling and sequencing – single server and multiple server models - deterministic inventory models - Probabilistic inventory control models - Geometric Programming

UNIT-V :

Competitive Models, Single and Multi-channel Problems, Sequencing Models, Dynamic Programming, Flow in Networks, Elementary Graph Theory, Game Theory Simulation

References:

1. H.A.Taha,Operations Research,AnIntroduction,PHI,2008
2. H.M.Wagner,Principles of Operations Research, PHI,Delhi,1982.
3. J.C.Pant, Introduction to Optimisation: Operations Research, Jain Brothers,Delhi,2008
4. Hitler Liber mann Operations Research :McGraw HillPub. 2009
5. Panner selvam,Operations Research:Prentice Hall of India 2010
6. Harvey MW agner,Principles of Operations Research :Prentice Hall of India 2010

Course Outcomes:

At the end of the course ,the student should be able to

1. Students should able to apply the dynamic programming to solve problems of discrete and continuous variables.
2. Students should able to apply the concept of non-linear programming
3. Students should able to carry out sensitivity analysis
4. Student should able to model the real world problem and simulate it.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II Year – III Semester	Course Code: MT24DE2107	L	T	P	C
		3	0	0	3
COST MANAGEMENT OF ENGINEERING PROJECTS (OPEN ELECTIVES)					

Introduction and Over view of the Strategic Cost Management Process

Cost concepts in decision-making; Relevant cost, Differential cost, Incremental cost and Opportunity cost. Objectives of a Costing System; Inventory valuation; Creation of a Database for operational control; Provision of data for Decision-Making.

Project: meaning, Different types, why to manage, cost overruns centres, various stages of project **execution:** conception to commissioning. Project execution as conglomeration of technical and non- technical activities. Detailed Engineering activities. Pre project execution main clearances and documents Project team: Role of each member. Importance Project site: Data required with significance. Project contracts. Types and contents. Project execution Project cost control. Bar charts and Network diagram. Project commissioning: mechanical and process

Cost Behavior and Profit Planning Marginal Costing; Distinction between Marginal Costing and Absorption Costing; Break-even Analysis, Cost-Volume-Profit Analysis. Various decision-making problems. Standard costing and Variance Analysis. Pricing strategies: Pareto Analysis. Target costing, Life Cycle Costing. Costing of service sector. Just-in-time approach, Material Requirement Planning, Enterprise Resource Planning, Total Quality Management and Theory of constraints. Activity-Based Cost Management, Bench Marking; Balanced Score Card and Value- Chain Analysis. Budgetary Control; Flexible Budgets; Performance budgets; Zero-based budgets. Measurement of Divisional profitability pricing decisions including transfer pricing.

Quantitative techniques for cost management, Linear Programming, PERT/CPM, Transportation problems, Assignment problems, Simulation, Learning Curve Theory.

References:

1. Cost Accounting A Managerial Emphasis, Prentice Hall of India, New Delhi
2. Charles T. Horngren and George Foster, Advanced Management Accounting
3. Robert S. Kaplan Anthony A. Alkinson, Management & Cost Accounting
4. Ashish K. Bhattacharya, Principles & Practices of Cost Accounting A.H. Wheeler publisher
5. N.D. Vohra, Quantitative Techniques in Management, Tata Mc Graw Hill Book Co. Ltd.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II Year – III Semester	Course Code: MT24DE2108	L	T	P	C
		3	0	0	3
COMPOSITE MATERIALS (OPEN ELECTIVES)					

UNIT–I:

INTRODUCTION: Definition – Classification and characteristics of Composite materials. Advantages and application of composites. Functional requirements of reinforcement and matrix. Effect of reinforcement (size, shape, distribution, volume fraction) on overall composite performance.

UNIT– II:

REINFORCEMENTS: Preparation-layup, curing, properties and applications of glass fibers, carbon fibers, Kevlar fibers and Boron fibers. Properties and applications of whiskers, particle reinforcements. Mechanical Behavior of composites: Rule of mixtures, Inverse rule of mixtures. Isostrain and Isostress conditions.

UNIT– III:

Manufacturing of Metal Matrix Composites: Casting – Solid State diffusion technique, Cladding – Hot isostatic pressing. Properties and applications. **Manufacturing of Ceramic Matrix Composites:** Liquid Metal Infiltration – Liquid phase sintering. **Manufacturing of Carbon – Carbon composites:** Knitting, Braiding, Weaving. Properties and applications.

UNIT–IV:

Manufacturing of Polymer Matrix Composites: Preparation of Moulding compounds and prepregs – hand layup method – Autoclave method – Filament winding method – Compression moulding – Reaction injection moulding. Properties and applications.

UNIT– V:

Strength: Laminar Failure Criteria-strength ratio, maximum stress criteria, maximum strain criteria, interacting failure criteria, hygro thermal failure. Laminate first ply failure-insight strength; Laminate strength-ply discount truncated maximum strain criterion; strength design using caplet plots; stress concentrations.

TEXT BOOKS:

1. Material Science and Technology –Vol13–Composites by R.W.Cahn–VCH, West Germany.
2. Materials Science and Engineering, An introduction. W.D Callister, Jr., Adapted by R. Bala subramaniam, John Wiley & Sons, NY, Indian edition, 2007.

References:

1. Hand Book of Composite Materials-ed-Lubin.
2. Composite Materials –K.K.Chawla.
3. Composite Materials Science and Applications–Deborah D.L.Chung.
4. Composite Materials Design and Applications –Danial Gay, Suong V.Hoa, and Stephen W. Tasi.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II Year – III Semester	Course Code: MT24DE2109	L	T	P	C
		3	0	0	3
WASTE TO ENERGY (OPEN ELECTIVES)					

Unit-I:

Introduction to Energy from Waste: Classification of waste as fuel – Agro based, Forest residue, Industrial waste-MSW–Conversion devices–Incinerators, gasifiers, digestors

Unit-II:

Biomass Pyrolysis: Pyrolysis–Types, slow fast–Manufacture of charcoal–Methods-Yields and application – Manufacture of pyrolytic oils and gases, yields and applications.

Unit-III:

Biomass Gasification: Gasifiers – Fixed bed system – Down draft and updraft gasifiers – Fluidized bed gasifiers – Design, construction and operation – Gasifier burner arrangement for Thermal heating–Gasifier engine arrangement and electrical power–Equilibrium and kinetic consideration in gasifier operation

Unit-IV:

Biomass Combustion: Biomass stoves – Improved chullahs, types, some exotic designs, Fixed bed combustors, Types, inclined grate combustors, Fluidized bed combustors, Design, construction and operation - Operation of all the above biomass combustors.

Unit-V:

Biogas: Properties of biogas (Calorific value and composition)- Biogas plant technology and status- Bio energy system - Design and constructional features - Biomass resources and their classification - Biomass conversion processes - Thermo chemical conversion -Direct combustion -biomass gasification
- pyrolysis and liquefaction - biochemical conversion - anaerobic digestion - Types of biogas Plants – Applications - Alcohol production from biomass - Bio diesel production - Urban waste to energy conversion - Biomass energy programme in India.

References:

1. Non Conventional Energy, Desai, Ashok V., Wiley Eastern Ltd., 1990.
2. Biogas Technology - A Practical Hand Book -Khandelwal, K. C. and Mahdi, S. S., Vol. I & II, Tata McGraw Hill Publishing Co. Ltd., 1983.
3. Food, Feed and Fuel from Biomass, Challal, D.S., IBH Publishing Co. Pvt. Ltd., 1991.
4. Biomass Conversion and Technology, C. Y. Wereko-Brobby and E. B. Hagan, John Wiley & Sons, 1996.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

II Year – III Semester	Course Code: MT24DE2110	L	T	P	C
		0	0	20	10
(DISSERTATION) DISSERTATION PHASE-I AND PHASE-II					

Syllabus Contents:

The dissertation / project topic should be selected / chosen to ensure the satisfaction of the urgent need to establish a direct link between education, national development and productivity and thus reduce the gap between the world of work and the world of study. The dissertation should have the following

- Relevance to social needs of society
- Relevance to value addition to existing facilities in the institute
- Relevance to industry need
- Problems of national importance
- Research and development in various

domain The student should complete the following:

- Literature survey Problem Definition
- Motivation for study and Objectives
- Preliminary design/feasibility/modular approaches
- Implementation and Verification
- Report and presentation

The dissertation stage II is based on a report prepared by the students on dissertations allotted to them. It may be based on:

- Experimental verification/ Proof of concept.
- Design, fabrication, testing of Communication System.
- The viva-voce examination will be based on the above report and work.

Guidelines for Dissertation Phase –I and II at M.Tech. (Electronics):

- As per the AICTE directives, the dissertation is a yearlong activity, to be carried out and evaluated in two phases i.e. Phase – I: July to December and Phase – II: January to June.
- The dissertation may be carried out preferably in-house i.e. department's laboratories and centers OR in industry allotted through department's T & P coordinator.
- After multiple interactions with guide and based on comprehensive literature survey, the student shall identify the domain and define dissertation objectives. The referred literature should preferably include IEEE/IET/IETE/Springer/Science Direct/ACM journals in the areas of Computing and Processing (Hardware and Software), Circuits-Devices and Systems, Communication-Networking and Security, Robotics and Control Systems, Signal Processing and Analysis and any other related domain. In case of Industry sponsored projects, the relevant application notes, white papers, product catalogues should be referred and reported.
- Student is expected to detail out specifications, methodology, resources required, critical issues involved in design and implementation and phase wise work distribution, and submit the proposal within a month from the date of registration.
- Phase – I deliverables: A document report comprising of summary of literature survey, detailed objectives, project specifications, paper and/or computer aided design, proof of concept/functionality, part results, A record of continuous progress.
- Phase – I evaluation: A committee comprising of guides of respective specialization shall



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assess the progress/performance of the student based on report, presentation and Q &A. In case of unsatisfactory performance, committee may recommend repeating the Phase-I work.

- During phase – II, student is expected to exert on design, development and testing of the proposed work as per the schedule. Accomplished results/contributions/innovations should be published in terms of research papers in reputed journals and reviewed focused conferences OR IP/Patents.
- Phase – II deliverables: A dissertation report as per the specified format, developed system in the form of hardware and/or software, a record of continuous progress.
- Phase–II evaluation: Guide along with appointed external examiner shall assess the progress/performance of the student based on report, presentation and Q &A. In case of unsatisfactory performance, committee may recommend for extension or repeating the work

Course Outcomes:

At the end of this course, students will be able to

1. Ability to synthesize knowledge and skills previously gained and applied to an in-depth study and execution of new technical problem.
2. Capable to select from different methodologies, methods and forms of analysis to produce a suitable research design, and justify their design.
3. Ability to present the findings of their technical solution in a written report.
4. Presenting the work in International/National conference or reputed journals.



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II Year – III Semester	Course Code: MT24DE2201	L	T	P	C
		0	0	32	16
(DISSERTATION) DISSERTATION PHASE–I AND PHASE–II					

Syllabus Contents:

The dissertation / project topic should be selected / chosen to ensure the satisfaction of the urgent need to establish a direct link between education, national development and productivity and thus reduce the gap between the world of work and the world of study. The dissertation should have the following

- Relevance to social needs of society
- Relevance to value addition to existing facilities in the institute
- Relevance to industry need
- Problems of national importance
- Research and development in various

domain The student should complete the following:

- Literature survey Problem Definition
- Motivation for study and Objectives
- Preliminary design/ feasibility/modular approaches
- Implementation and Verification
- Report and presentation

The dissertation stage II is based on are port prepared by the students on dissertation allotted to them.It may be based on:

- Experimental verification/ Proof of concept.
- Design,fabrication, testing of Communication System.
- The viva-voce examination will be based on the above report and work.

Guidelines for Dissertation Phase –I and II at M. Tech. (Electronics):

- As per the AICTE directives, the dissertation is a yearlong activity, to be carried out and evaluated in two phases i.e. Phase – I: July to December and Phase – II: January to June.
- The dissertation may be carried out preferably in-house i.e. department’s laboratories and centers OR in industry allotted through department’s T & P coordinator.
- After multiple interactions with guide and based on comprehensive literature survey, the student shall identify the domain and define dissertation objectives. The referred literature should preferably include IEEE/IET/IETE/Springer/Science Direct/ACM journals in the areas of Computing and Processing (Hardware and Software), Circuits-Devices and Systems, Communication-Networking and Security, Robotics and Control Systems, Signal Processingand Analysis and any other related domain. In case of Industry sponsored projects, the relevant application notes, while papers, product catalogues should be referred and reported.



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- Student is expected to detail out specifications, methodology, resources required, critical issues involved in design and implementation and phase wise work distribution, and submit the proposal within a month from the date of registration.
- Phase – I deliverables: A document report comprising of summary of literature survey, detailed objectives, project specifications, paper and/or computer aided design, proof of concept/functionality, part results, A record of continuous progress.
- Phase – I evaluation: A committee comprising of guides of respective specialization shall assess the progress/performance of the student based on report, presentation and Q &A. In case of unsatisfactory performance, committee may recommend repeating the Phase-I work.



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- During phase – II, student is expected to exert on design, development and testing of the proposed work as per the schedule. Accomplished results/contributions/innovations should be published in terms of research papers in reputed journals and reviewed focused conferences OR IP/Patents.
- Phase – II deliverables: A dissertation report as per the specified format, developed system in the form of hardware and/or software, a record of continuous progress.
- Phase–IIevaluation:Guidealongwithappointedexternalexaminershallassessthe progress/performance of the student based on report, presentation and Q &A. In case of unsatisfactoryperformance,committeemayrecommendforextensionorrepeatingthework

Course Outcomes:

At the end of this course, students will be able to

1. Ability to synthesize knowledge and skills previously gained and applied to an in-depth study and execution of new technical problem.
2. Capable to select from different methodologies, methods and forms of analysis to produce a suitable research design, and justify their design.
3. Ability to present the findings of their technical solution in a written report.
4. Presenting the work in International/National conference or reputed journals.



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – I Semester	Course Code: MT24DE1112	L	T	P	C
		2	0	0	0
AUDIT 1 and 2: ENGLISH FOR RESEARCH PAPER WRITING					

Course objectives: Students will be able to:		
<ul style="list-style-type: none"> • Understand that how to improve your writing skills and level of read ability. Learn about what to write in each section. • Understand the skills needed when writing a Title Ensure the good quality of paper at very first- time submission. 		
Syllabus		
Units	CONTENTS	Hours
1	Planning and Preparation, Word Order, Breaking up long sentences, Structuring Paragraphs and Sentences, Being Concise And Removing Redundancy, Avoiding Ambiguity and Vagueness	4
2	Clarifying Who Did What, Highlighting Your Findings, Hedging and Criticizing, Paraphrasing and Plagiarism, Sections of a Paper, Abstracts. Introduction	4
3	Review of the Literature, Methods, Results, Discussion, Conclusions, The Final Check.	4
4	Key skills are needed when writing a Title, key skills are needed when writing an Abstract, key skills are needed when writing an Introduction, skills needed when writing a Review of the Literature,	4
5	Skills are needed when writing the Methods, skills needed when Writing the Results, skills are needed when writing the Discussion, skills are needed when writing the Conclusions	4
6	Useful phrases, how to ensure paper is as good as it could possibly Be the first-time submission	4

Suggested Studies:

1. Gold bortR (2006)Writing for Science, Yale University Press(available on Google Books)
2. DayR (2006) How to Write and Publish a Scientific Paper, Cambridge University Press
3. Highman N (1998), Handbook of Writing for the Mathematical Sciences,SIAM. Highman’s book.
4. Adrian Wallwork , English for Writing Research Papers, Springer New York Dordrecht Heidelberg London, 2011



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

AUDIT 1 and 2: DISASTER MANAGEMENT

Course Objectives:- Students will be able to:

- Learn to demonstrate a critical understanding of key concepts in disaster risk reduction and human it Arian response.
- Critically evaluate disaster risk reduction and human it Arian response policy and practice from multiple perspectives.
- Develop an understanding of standards of human it Arian response and practical relevance in specific types of disasters and conflict situations.
- Critically understand the strengths and weaknesses of disaster management approaches, planning and programming in different countries, particularly their home country or the countries they work in

Syllabus

Units	CONTENTS	Hours
1	Introduction Disaster: Definition, Factors And Significance; Difference Between Hazard And Disaster; Natural And Manmade Disasters: Difference, Nature, Types And Magnitude.	4
2	Repercussions Of Disasters And Hazards: Economic Damage, Loss Of Human And Animal Life, Destruction Of Eco system. Natural Disasters: Earthquakes, Volcanisms, Cyclones, Tsunamis, Floods, Droughts And Famines, Landslides And Avalanches, Man-made disaster: Nuclear Reactor Meltdown, Industrial Accidents, Oil Slicks And Spills, Outbreaks Of Disease And Epidemics, War And Conflicts.	4
3	Disaster Prone Areas In India Study Of Seismic Zones; Areas Prone To Floods And Droughts, Landslides And Avalanches; Areas Prone To Cyclonic And Coastal Hazards With Special Reference To Tsunami; Post-Disaster Diseases And Epidemics	4
4	Disaster Preparedness And Management Preparedness: Monitoring Of Phenomena Triggering A Disaster Or Hazard; Evaluation Of Risk: Application Of Remote Sensing, Data From Meteorological And Other Agencies, Media Reports: Governmental And Community Preparedness.	4



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5	Risk Assessment Disaster Risk: Concept And Elements, Disaster Risk Reduction, Global And National Disaster Risk Situation. Techniques Of Risk Assessment, Global Co-Operation In Risk Assessment And Warning, People's Participation In Risk Assessment. Strategies for Survival.	4
6	Disaster Mitigation Meaning, Concept And Strategies Of Disaster Mitigation, Emerging Trends In Mitigation. Structural Mitigation And Non-Structural Mitigation, Programs Of Disaster Mitigation In India.	4
Suggested Readings: <ol style="list-style-type: none">1. R. Nishith, Singh A K, "Disaster Management in India: Perspectives, issues and strategies""New Royal book Company.2. Sahni, Pardeep Et.Al. (Eds.)," Disaster Mitigation Experiences and Reflections", Prentice Hall Of India, New Delhi.3. Goel S. L., Disaster Administration and Management Text and Case Studies", Deep & Deep Publication Pvt. Ltd., New Delhi.		



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

AUDIT 1 and 2: SANSKRIT FOR TECHNICAL KNOWLEDGE

Course Objectives

1. To get a working knowledge illustrious Sanskrit, the scientific language in the world.
2. Learning of Sanskrit to improve brain functioning.
3. Learning of Sanskrit to develop the logic in mathematics, science & other subjects enhancing the memory power.
4. The engineering scholars equipped with Sanskrit will be able to explore the huge knowledge from ancient literature.

Syllabus

Unit	Content	Hours
1	<ul style="list-style-type: none">• Alphabets in Sanskrit,• Past/Present/Future Tense,• Simple Sentences	8
2	<ul style="list-style-type: none">• Order• Introduction of roots• Technical information about Sanskrit Literature	8
3	<ul style="list-style-type: none">• Technical concepts of Engineering-Electrical, Mechanical, Architecture, Mathematics	8

Suggested reading

1. “Abhyaspustakam”–Dr.Vishwas, Samskrita-Bharti Publication, New Delhi
2. “Teach Yourself Sanskrit” Prathama Deeksha- Vempati Kutumbshastri, Rashtriya Sanskrit Sansthanam, New Delhi Publication
3. “India”s Glorious Scientific Tradition” Suresh Soni, Ocean books (P) Ltd., New Delhi.

Course Output

Students will be able to

1. Understanding basic Sanskrit language
2. Ancient Sanskrit literature about science & technology can be understood
3. Being a logical language will help to develop logic in students



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AUDIT 1 and 2: VALUE EDUCATION

Course Objectives

Students will be able to

1. Understand value of education and self- development
2. Imbibe good values in students
3. Let the should know about the importance of character

Syllabus

Unit	Content	Hours
1	<ul style="list-style-type: none">• Values and self-development –Social values and individual attitudes.• Work ethics, Indian vision of humanism.• Moral and non-moral valuation. Standards and principles.• Value judgments	4
2	<ul style="list-style-type: none">• Importance of cultivation of values.• Sense of duty. Devotion, Self-reliance. Confidence, Concentration. Truthfulness, Cleanliness.• Honesty, Humanity. Power of faith, National Unity.• Patriotism. Love for nature, Discipline	6
3	<ul style="list-style-type: none">• Personality and Behavior Development - Soul and Scientific attitude. Positive Thinking. Integrity and discipline.• Punctuality, Love and Kindness.• Avoid fault Thinking.• Free from anger, Dignity of labor.• Universal brother hood and religious tolerance.• True friendship.• Happiness Vs suffering, love for truth.• Aware of self-destructive habits.• Association and Cooperation.• Doing best for saving nature	6
4	<ul style="list-style-type: none">• Character and Competence–Holy books vs Blind faith.• Self-management and Good health.• Science of reincarnation.• Equality, Non violence, Humility, Role of Women.• All religions and same message.• Mind your Mind, Self-control.• Honesty, Studying effectively	6

Suggested reading

1. Chakroborty, S.K. “Values and Ethics for organizations Theory and practice”, Oxford University Press, New Delhi

Course outcomes

Students will be able to

1. Knowledge of self-development



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2. Learn the importance of Human values
3. Developing the overall personality



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

I Year – II Semester	Course Code: MT24DE1212	L	T	P	C
		2	0	0	0
AUDIT 1 and 2: CONSTITUTION OF INDIA					

Course Objectives:

- Students will be able to:
- Understand the premises informing the twin themes of liberty and freedom from a civil rights perspective.
- To address the growth of Indian opinion regarding modern Indian intellectuals' constitutional role and entitlement to civil and economic rights as well as the emergence of nationhood in the early years of Indian nationalism.
- To address the role of socialism in India after the commencement of the Bolshevik Revolution in 1917 and its impact on the initial drafting of the Indian Constitution.

Syllabus

Units	Content	Hour s
1	<ul style="list-style-type: none"> • History of Making of the Indian Constitution: History Drafting Committee, (Composition & Working) 	4
2	<ul style="list-style-type: none"> • Philosophy of the Indian Constitution: Preamble Salient Features 	4
3	<ul style="list-style-type: none"> □ Contours of Constitutional Rights & Duties: □ Fundamental Rights □ Right to Equality □ Right to Freedom □ Right against Exploitation □ Right to Freedom of Religion □ Cultural and Educational Rights □ Right to Constitutional Remedies □ Directive Principles of State Policy □ Fundamental Duties. 	4
4	<ul style="list-style-type: none"> □ Organs of Governance: □ Parliament □ Composition □ Qualifications and Disqualifications □ Powers and Functions • Executive □ President □ Governor 	4



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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

	<input type="checkbox"/> Council of Ministers <input type="checkbox"/> Judiciary, Appointment and Transfer of Judges, Qualifications <input type="checkbox"/> Powers and Functions	
5	<input type="checkbox"/> Local Administration: <input type="checkbox"/> District's Administration head: Role and Importance, <input type="checkbox"/> Municipalities: Introduction, Mayor and role of Elected Representative, CE of Municipal Corporation. <input type="checkbox"/> Pachayati raj: Introduction, PRI: Zila Pachayat. <input type="checkbox"/> Elected officials and their roles, CEO Zila Pachayat: Position and role. <input type="checkbox"/> Block level: Organizational Hierarchy (Different departments), <input type="checkbox"/> Village level: Role of Elected and Appointed officials, <input type="checkbox"/> Importance of grass root democracy	4
6	<input type="checkbox"/> Election Commission: <input type="checkbox"/> Election Commission: Role and Functioning. <input type="checkbox"/> Chief Election Commissioner and Election Commissioners. <input type="checkbox"/> State Election Commission: Role and Functioning. <input type="checkbox"/> Institute and Bodies for the welfare of SC/ST/OBC and women.	4

Suggested reading

1. The Constitution of India, 1950 (Bare Act), Government Publication.
2. Dr.S.N.Busi, Dr.B.R.Ambedkar framing of Indian Constitution, 1st Edition, 2015.
3. M.P.Jain, Indian Constitution Law, 7th Edn., Lexis Nexis, 2014.
4. D.D. Basu, Introduction to the Constitution of India, Lexis Nexis, 2015.

Course Outcomes:

Students will be able to:

1. Discuss the growth of the demand for civil rights in India for the bulk of Indians before the arrival of Gandhi in Indian politics.
2. Discuss the intellectual origins of the framework of argument that informed the conceptualization of social reforms leading to revolution in India.
3. Discuss the circumstances surrounding the foundation of the Congress Socialist Party [CSP] under the leadership of Jawaharlal Nehru and the eventual failure of the proposal of direct elections through adult suffrage in the Indian Constitution.
4. Discuss the passage of the Hindu Code Bill of 1956.



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AUDIT 1 and 2: PEDAGOGY STUDIES		
Course Objectives:		
Students will be able to:		
1. Review existing evidence on the review topic to inform programmed design and policy making undertaken by the Df ID, other agencies and researchers.		
2. Identify critical evidence gaps to guide the development.		
Syllabus		
Units	Content	Hours
1	<ul style="list-style-type: none"> • Introduction and Methodology: • Aims and rationale, Policy background, Conceptual framework and terminology. • Theories of learning, Curriculum, Teacher education. • Conceptual frame work, Research questions. • Overview of methodology and Searching. 	4
2	<ul style="list-style-type: none"> • Thematic overview: Pedagogical practices are being used by teachers in formal and informal classrooms in developing countries. • Curriculum, Teacher education. 	2
3	<ul style="list-style-type: none"> • Evidence on the effectiveness of pedagogical practices • Methodology for the in depth stage: quality assessment of included studies. • How can teacher education (curriculum and practicum) and the school • Curriculum and guidance materials best support effective pedagogy? • Theory of change. • Strength and nature of the body of evidence for effective pedagogical practices. • Pedagogic theory and pedagogical approaches. • Teachers' attitudes and beliefs and Pedagogic strategies. 	4
4	<ul style="list-style-type: none"> • Professional development: alignment with class room practices and follow-up support • Peer support • Support from the head teacher and the community. • Curriculum and assessment • Barriers to learning: limited resources and large class sizes 	4
5	<ul style="list-style-type: none"> • Research gaps and future directions • Research design • Contexts • Pedagogy • Teacher education • Curriculum and assessment • Dissemination and research impact. 	2



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Suggested reading

1. Ackers J, Hardman F (2001) Classroom interaction in Kenyan primary schools, Compare, 31 (2): 245-261.
2. Agrawal M (2004) Curricular reform in schools: The importance of evaluation, Journal of Curriculum Studies, 36 (3): 361-379.
3. Akyeampong K(2003) Teacher training in Ghana-does it count? Multi-site teacher education research project (MUSTER) country report 1. London: DFID.
4. Akyeampong K, Lussier K, Pryor J, Westbrook J (2013) Improving teaching and learning of basic maths and reading in Africa: Does teacher preparation count? International Journal Educational Development, 33 (3): 272–282.
5. Alexander RJ(2001)Culture and pedagogy:International comparisons in primary education. Oxford and Boston: Blackwell.
6. ChavanM(2003)ReadIndia:Amassscale,rapid,,,learningtoread“campaign.
7. www.pratham.org/images/resource%20working%20paper%202.pdf.

Course Outcomes:

Students will be able to understand:

1. What pedagogical practices are being used by teachers informal and informal classrooms
In developing countries?
2. What is the evidence on the effectiveness of these pedagogical practices, in what conditions, and with what population of learners?
3. How can teacher education (curriculum and practicum)and the school curriculum and guidance materials best support effective pedagogy?



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AUDIT 1 and 2: STRESS MANAGEMENT BY YOGA

Course Objectives

1. To achieve overall health of body and mind
2. To overcome stress

Syllabus

Unit	Content	Hours
1	<ul style="list-style-type: none">• Definitions of Eight parts of yog. (Ashtanga)	8
2	<ul style="list-style-type: none">• Yam and Niyam. Do's and Don'ts in life.• Ahinsa, satya, astheya, bramhacharya and aparigraha• Shaucha, santosh, tapa, swadhyay, ishwarpranidhan	8
3	<ul style="list-style-type: none">• Asan and Pranayam• Various yogasanas and their benefits for mind & body• Regularization of breathing techniques and its effects-Types of pranayam	8
<p>Suggested reading</p> <ol style="list-style-type: none">1. „Yogic Asanas for Group Training-Part-I”: Janardan Swami Yoga bhyasi Mandal, Nagpur2. “Rajayoga or conquering the Internal Nature” by Swam <p>Course Outcomes: Students will be able to:</p> <ol style="list-style-type: none">1. Develop healthy mind in a healthy body thus improving social health also2. Improve efficiency		



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**AUDIT 1 and 2: PERSONALITY DEVELOPMENT THROUGH LIFE
ENLIGHTENMENT SKILLS**

Course Objectives

1. To learn to achieve the highest goal happily
2. To become a person with stable mind, pleasing personality and determination
3. To awaken wisdom in students

Syllabus

Unit	Content	Hours
1	Neetisatakam –Holistic development of personality <ul style="list-style-type: none">• Verses-19,20,21,22 (wisdom)• Verses-29,31,32(pride &heroism)• Verses-26,28,63,65 (virtue)• Verses-52,53,59(dont's)• Verses-71,73,75,78(do's)	8
2	<ul style="list-style-type: none">• Approach today work and duties.• ShrimadBhagwadGeeta:Chapter2-Verses41,47,48,• Chapter3-Verses13,21,27,35,Chapter6-Verses5,13,17,23,35,• Chapter18-Verses45,46,48.	8
3	<ul style="list-style-type: none">• Statements of basic knowledge.• ShrimadBhagwadGeeta:Chapter2-Verses56,62,68• Chapter12-Verses 13,14, 15,16,17, 18• Personality of Rolemodel.Shrimad Bhagwad Geeta:Chapter2-• Verses 17, Chapter 3-Verses 36,37,42,• Chapter4-Verses18,38,39• Chapter18–Verses 37,38,63	8

Suggested reading

1. "Srimad Bhagavad Gita"by Swami Swarupananda Advaita Ashram (Publication Department), Kolkata
2. Bhartrihari's ThreeSatakam(Niti-sringar-vairagya)byP.Gopinath,RashtriyaSanskrit Sansthanam, New Delhi.

Course Outcomes

Students will be able to

1. StudyofShrimad-Bhagwad-Geetawillhelpthestudentindevelopinghispersonalityand achieve the



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highest goal in life

2. The person who has studied Geeta will lead the nation and mankind to peace and prosperity
3. Study of Neetishatakam will help in developing versatile personality of students.