BUZZ
ece

A VOICE OF ELECTRONICS AND COMMUNICATION ENGINEERING

DEPARTMENT OF ELECTRONICS & COMMUNICATION

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VOLUME III ISSUE I

DEPARTMENT VISION & MISSION

VISION

To be a recognized center for innovation in Electronics & Communication Engineering with ethics in research and serving society.

MISSION

DM1: Impart knowledge skills on state-of art technologies aligned to address industry and society needs.

DM2: Organize activities to inculcate self-learning lifelong learning, team spirit and professional ethics.

DM3: Provide quality environment, promoting research innovation and entrepreneur skills.

Program Outcomes (PO's)

PO1: Engineering knowledge: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.

PO2: Problem analysis: Identify, formulate, review research literature, and analyze complex engineering   problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.

PO3: Design/development of solutions: Ability and skills to effectively use state-of-the-art techniques and computing tools for analysis, design and implementation of computing systems which resolve real life problems.

PO4: Conduct investigations of complex problems: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.

PO5: Modern tool usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.

PO6: The engineer and society: Apply reasoning informed by contextual knowledge to assess societal, health, safety, legal and cultural issues, and the consequent responsibilities relevant to the professional engineering practice.

PO7: Environment and sustainability: Understand the impact of professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.

PO8: Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of engineering practice.

PO9: Individual and teamwork: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.

PO10: Communication: Make effective presentations and give and receive clear instructions. with society at large. Be able to comprehend and write effective reports documentation.

PO11: Project management and finance: Demonstrate knowledge and understanding of engineering and management principles and apply these to one's own work, as a member and leader in a team. Manage projects in multidisciplinary environments.

PO12: Life-long learning: Recognize the need for and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

Programme Educational Objectives (PEOs)

PEO1: Demonstrate the educational foundation needed for professional career/higher studies in the field of Electronics and Communication Engineering

PEO2:Provide solutions for the real time problems with the ever-changing industry requirements.

PEO3: Develop attitude for life long learning and practice the profession with integrity and responsibility

Program Specific Outcome's (PSO's)

PSO1: Design and provide solutions in Power Electronics and Power Systems.

PSO2: Demonstrate renewable energy technologies for growing energy demand.

FACULTY ACHIEVEMENTS

- ➢ Mrs. N S V L Sowjanya Got Elite in the Course of Digital Circuits in 12 week course Jul-Oct 2021 with score of 70% .

➢ Dr. N. Venkata Rao Got Elite in the Course of Microwave Theory and Techniques in NPTEL-IIT BOMBAY JUL-OCT 2021

EVENTS ATTENDED -FACULTY

➢ Dr. Nekkanti Venkata Rao attended Microwave Theory and Techniques NPTEL-IIT BOMBAY JUL-OCT 2021 12 WEEKS.

➢ .Mr. Kopalli Venkanna Naidu attended FDP RF Energy Harvesting Antenna Design for Wireless Body Area Networks: Design, Development, and Challenges in Sri Vasavi Engineering College Tadepalligudem from 23/08/2021 To 27/08/2021

➢ Mr. Kopalli Venkanna Naidu attended FDP Recent Trends and Applications of GIS & Remote Sensing using AI in Potti Sriramulu Chalavadi Mallikarjuna Rao College of Engineering & Technology Vijayawada from 13/09/2021 To 17/09/2021

➢ Mrs. N. Maryleena attended FDP RF Energy Harvesting Antenna Design for Wireless Body Area Networks: Design, Development, and Challenges Sri Vasavi Engineering College Tadepalligudem   from 23/08/2021 To 27/08/2021

➢ Mrs.N. Maryleena attended FDP Sensors Applications Through Internet of Things University B D T College of Engineering (Constitution College of VTU Belagavi) BELAGAVI from 20/09/2021 To 24/09/2021

➢ Mr. Srinivas Yallapu attended FDP Recent Trends and Applications of GIS & Remote Sensing using AI Potti Sriramulu Chalavadi Mallikarjuna Rao College of Engineering & Technology Vijayawada from 13/09/2021 To 17/09/2021

➢ Mrs.SVL Sowjanya Nukala attended FDP Recent Trends and Applications of GIS & Remote Sensing using AI Potti Sriramulu Chalavadi Mallikarjuna Rao College of Engineering & Technology Vijayawada from 13/09/2021 To 17/09/2021

- ➢ Mr. K Surya Satish Kumar attended FDP Recent Trends and Applications of GIS & Remote Sensing using AI Potti Sriramulu Chalavadi Mallikarjuna Rao College of Engineering & Technology Vijayawada 13/09/2021 To 17/09/2021

- ➢ Mr. K. Sekhar Babu attended FDP RF Energy harvesting Antenna Design for Wireless Body Area Networks: Design, Development, and ChallengesSri Vasavi Engineering College Tadepalligudem from 23/08/2021 To 27/08/2021

- ➢ Mrs.V. Bhavani Durga attended FDP Recent Trends in Nanotechnology & Signal Processing GMR Institute of Technology RAJAM from 23/08/2021 To 29/08/2021

EVENTS PARTICIPATION-STUDENTS

- ➢ Penmetsa Siva Gayatri attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- ➢ Kammila Reshi Kanth attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- ➢ Podugu Satish attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- ➢ Sanapathi Kusuma Kumari attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- ➢ Gariki Sony Rekha attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- ➢ Kunaparaju DeepikaLakshmi attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- ➢ K Kowsalya Devi attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- ➢ K Nikitha attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to

19/092021.

- Gopey Tejaswi attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- I Sandeep attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- Kothakota EswaraRao attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- Sudabattula Tanooj Kumar attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

- Tangeti Rajesh attended Workshop on "FPGA Implementation of Digital Circuits Using Xilinx Vivado" in Tirumala Engineering College, Narasaraopet from 15/09/2021 to 19/092021.

PLACEMENTS

- 6 students have been selected in MNC WIPRO with high package of 4LPA
- 2 students have been selected in OCULOTRONICS TECHNOLOGY PVT LTD with 2.4LPA

PROJECTS

External Project review has been conducted from 28/07/2021 to 30/07/2021 and the best projects has been selected as follows.

S.	Batch No.	Roll.No	Student Name	Name of the guide	Title of the project
1	17EC08	179P1A0425	JANAPAMULA SRAVANI	Mrs.N.mary Leena	Fetal Movement detection by using acceleration sensor and MEMS microphone
		179P1A0417	DUDE LAKSHMI SIRI JESHA		
		179P1A0449	Y SHAILU ELIZABETH		
		179P1A0432	K SAI VENKATA KRISHNA REDDY		
2	17EC09	179P1A0415	CHITIKINA DEDEEPYA	Mr.Y.Srinivas	IOT based smart agriculture monitoring system
		179P1A0425	KADIMI NAGA ROHIT		
		17MU1A0407	BOBBILI DHANA PRASANTHI		
		179P1A0440	MADDIPATI SATYA MOUNIKA		
3	17EC11	179P1A0420	GODAVARI VISHNU PRIYA	Mr.K.Sekhar Babu	Child rescue system from open bore well with Rasberry pi
		179P1A0422	GURUDASU VENKATRAO		
		17MU1A0417	Y THARAKA NAGA SRIRAM		
		17MU1A0419	ANNAMNEDI SURENDRA		
		17MU1A0409	GUDIMETLA SAIBABA REDDY		
4	17EC14	179P1A0458	PERICHARLA CHANDRA MOULI VARMA	Mr.V.Balaji	IOT based smart saline bottle for health care
		179P1A0428	KETHA VIJAYA BABU		
		179P1A0451	NIMMANA DURGA GANESH		
6	17EC15	17MU1A0410	K M M N V DURGA VARA PRASAD	Dr.S.Koteswari	IOT based accident avoidance using multi sensors
		179P1A0447	NAGA VARAPU ESWAR		
		179P1A0460	PUSAPATI DILEEP SAI VARMA		
		17MU1A0422	THONTA SRIRAM MURTHY		
7	17EC27	179P1A0480	TORATI JHANSI	Mrs.N.S.V.L. Sowjanya	Student Attendance recording and monitoring system using GSM and wireless sensor technology
		179P1A0490	YALLAPU PAVAN		
		189P5A0413	DWARAPUDI MEENAKSHI		
		189P5A0410	CH CHESAN RAM SAI ESWARARAO		

Project Coordinator   HOD